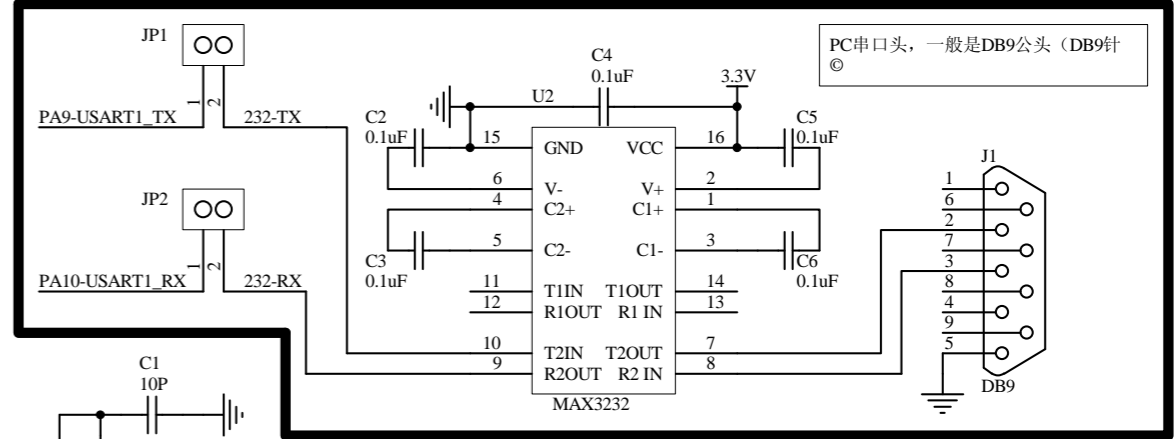


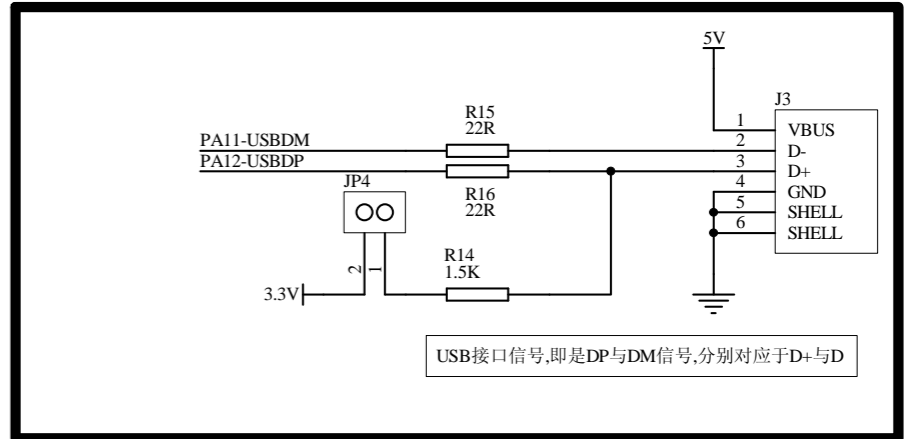
U1

| | | | | | |
|----------------|-----|-----------------|-----------------|----|----------------|
| PA0 | 23 | PA0-WKUP | PC0 | 15 | PC0 |
| PA1 | 24 | PA1 | PC1 | 16 | PC1 |
| PA2 | 25 | PA2 | PC2 | 17 | PC2 |
| PA3 | 26 | PA3 | PC3 | 18 | PC3 |
| PA4 | 29 | PA4 | PC4 | 33 | PC4 |
| PA5 | 30 | PA5 | PC5 | 34 | PC5 |
| PA6 | 31 | PA6 | PC6 | 63 | PC6 |
| PA7 | 32 | PA7 | PC7 | 64 | PC7 |
| PA8 | 67 | PA8 | PC8 | 65 | PC8 |
| PA9-USART1_TX | 68 | PA9 | PC9 | 66 | PC9 |
| PA10-USART1_RX | 69 | PA10 | PC10 | 78 | PC10 |
| PA11-USBDP | 70 | PA11 | PC11 | 79 | PC11 |
| PA12-USBDP | 71 | PA12 | PC12 | 80 | PC12 |
| PA13-JTMS | 72 | PA13/JTMS/SWDIO | PC13-TAMPER-RTC | 7 | PC13 |
| PA14-JTCK | 76 | PA14/JTCK/SWCLK | PC14-OSC32_IN | 8 | PC14-OSC32_IN |
| PA15-JTDI | 77 | PA15/JTDI | PC15-OSC32_OUT | 9 | PC15-OSC32_OUT |
| PB0 | 35 | PB0 | PD0 | 81 | PD0 |
| PB1 | 36 | PB1 | PD1 | 82 | PD1 |
| PB2-BOOT1 | 37 | PB2/BOOT1 | PD2 | 83 | PD2 |
| PB3-JTDO | 89 | PB3/JTDO | PD3 | 84 | PD3 |
| PB4-JNTRST | 90 | PB4/JNTRST | PD4 | 85 | PD4 |
| PB5 | 91 | PB5 | PD5 | 86 | PD5 |
| PB6 | 92 | PB6 | PD6 | 87 | PD6 |
| PB7 | 93 | PB7 | PD7 | 88 | PD7 |
| PB8-CAN_RX | 95 | PB8 | PD8 | 55 | PD8 |
| PB9-CAN_TX | 96 | PB9 | PD9 | 56 | PD9 |
| PB10-485_TX | 47 | PB10 | PD10 | 57 | PD10 |
| PB11-485_RX | 48 | PB11 | PD11 | 58 | PD11 |
| PB12 | 51 | PB12 | PD12 | 59 | PD12 |
| PB13 | 52 | PB13 | PD13 | 60 | PD13 |
| PB14 | 53 | PB14 | PD14 | 61 | PD14 |
| PB15 | 54 | PB15 | PD15 | 62 | PD15 |
| OSC-IN | 12 | OSC_IN | PE0 | 97 | PE0 |
| OSC-OUT | 13 | OSC_OUT | PE1 | 98 | PE1 |
| BOOT0 | 94 | BOOT0 | PE2 | 1 | PE2 |
| RESET | 14 | NRST | PE3 | 2 | PE3 |
| VSSA | 20 | VREF- | PE4 | 3 | PE4 |
| VDDA | 21 | VREF+ | PE5 | 4 | PE5 |
| NC | 73 | NC | PE6 | 5 | PE6 |
| VBAT | 6 | VBAT | PE7 | 38 | PE7 |
| VDD_1 | 50 | VDD_1 | PE8 | 39 | PE8 |
| VDD_2 | 75 | VDD_2 | PE9 | 40 | PE9 |
| VDD_3 | 100 | VDD_3 | PE10 | 41 | PE10 |
| VDD_4 | 28 | VDD_4 | PE11 | 42 | PE11 |
| VDD_5 | 11 | VDD_5 | PE12 | 43 | PE12 |
| VDDA | 22 | VDDA | PE13 | 44 | PE13 |
| | | | PE14 | 45 | PE14 |
| | | | PE15 | 46 | PE15 |
| | | | VSS_1 | 49 | VSS_1 |
| | | | VSS_2 | 74 | VSS_2 |
| | | | VSS_3 | 99 | VSS_3 |
| | | | VSS_4 | 27 | VSS_4 |
| | | | VSS_5 | 10 | VSS_5 |
| | | | VSSA | 19 | VSSA |

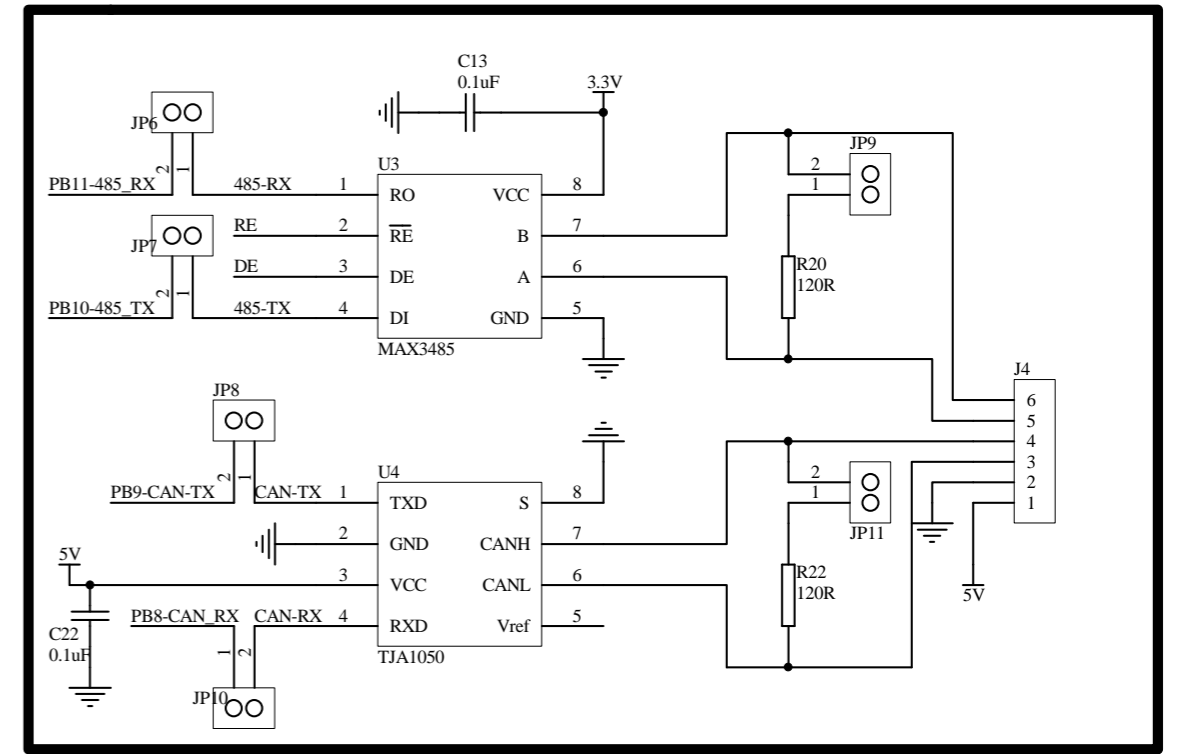
STM32F103VET6



PC串口头，一般是DB9公头 (DB9针)



USB接口信号,即是DP与DM信号,分别对应于D+与D-



| | | |
|-------|--------------------------------------------------------------|----------|
| Title | | |
| Size | Number | Revision |
| B | | |
| Date: | 10-Jun-2011 | Sheet of |
| File: | Da工程工程资料文件\开发板STM32F103VET6\STM32F103VET6电路图\STM32VET6\STM32 | |