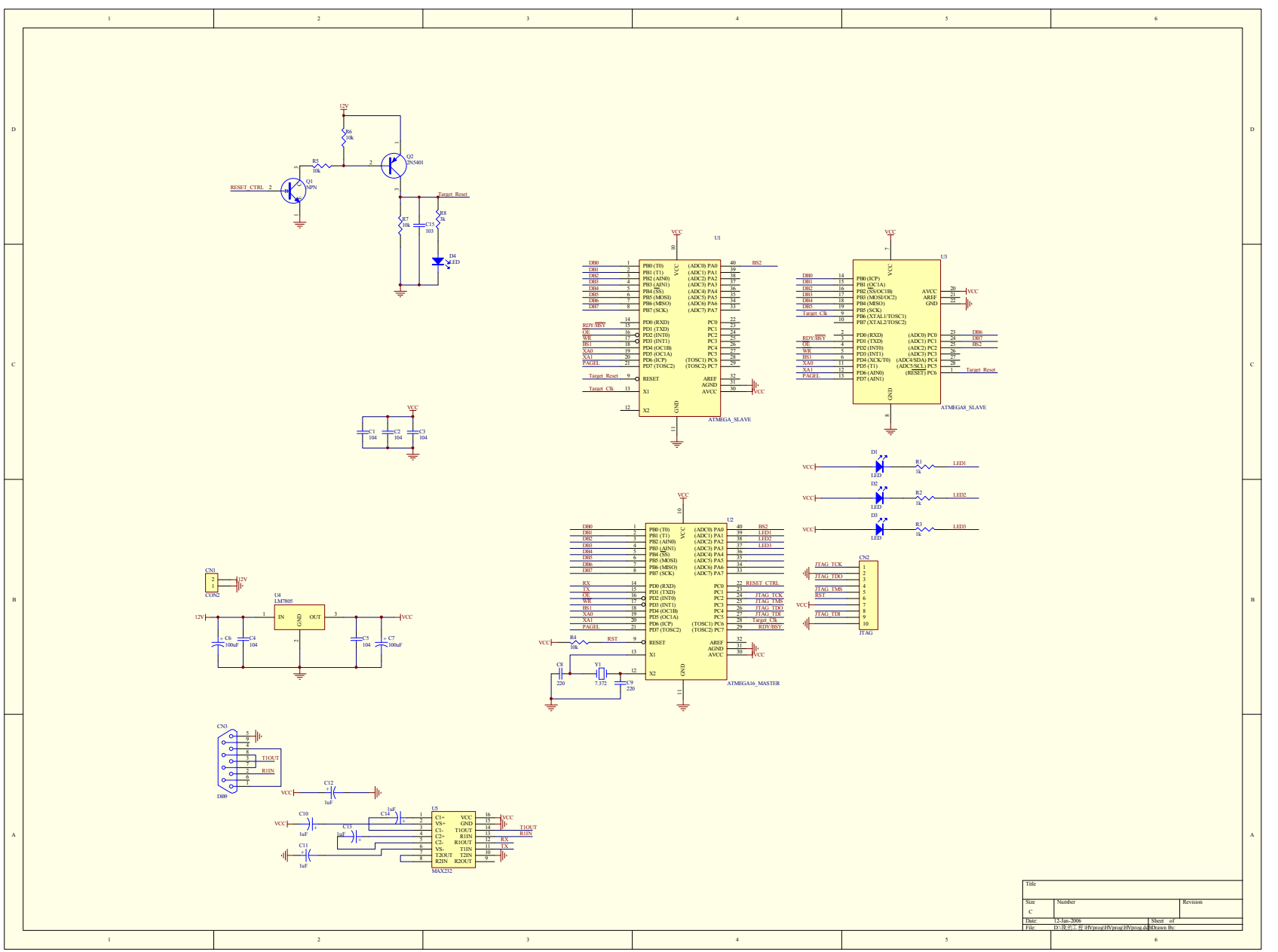

12V
R6 10k
R5 10k
Q2 2N5401
Q1 NPN
RESET_CTRL
Target_Reset
R7 10k
C15 103
R8 3k
D4 LED
VCC
U1
ATMEGA_SLAVE
U3
ATMEGA8_SLAVE
C1 104
C2 104
C3 104
D1 LED
D2 LED
D3 LED
R1 1k
R2 1k
R3 1k
LED1
LED2
LED3
U2
ATMEGA16_MASTER
CN1 CON2
U4 LM7805
IN
OUT
GND
C6 100uF
C4 104
C5 104
C7 100uF
CN2 JTAG
JTAG_TCK
JTAG_TDO
JTAG_TMS
RST
JTAG_TDI
R4 10k
C8 220
Y1 7.372
C9 220
CN3 DB9
T1OUT
R1IN
C12 1uF
C10 1uF
C11 1uF
C13 1uF
C14 1uF
U5 MAX232
C1+
VS+
C1-
C2+
C2-
VS-
T2OUT
R2IN
VCC
GND
T1OUT
R1IN
R1OUT
T1IN
T2IN
R2OUT
RX
TX
Title
Size C
Number
Revision
Date: 12-Jan-2006
Sheet of
File: D:\我的工程\HVprog\HVprog\HVprog.ddb
Drawn By: