

PCN / EOL Notification

☐ Quality/Reliability

Product Change Notification Number: RC103803			Notification Dat	Notification Date*: September 21, 2010	
Tit	le: AT91SAM7S256 MRL B F	Revision and Ordering Cod	e Change		
Pro	oduct Identification:				
	Current Part Number	New Part Number			
	AT91SAM7S256-AU-001	AT91SAM7S256C-AU			
	AT91SAM7S256-AU-999	AT91SAM7S256C-AU-99	99		
	AT91SAM7S256-MU	AT91SAM7S256C-MU			
	AT91SAM7S256-MU-999	AT91SAM7S256C-MU-99	99		
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Re	ason for Change:	⊠ Design	☐ Processing	☐ Logistics	

Change Description:

A complete list of differences between the marketing revision level (MRL) B and C can be found in chapter 40 of the datasheet of the AT91SAM7S-series with Atmel document reference 6175K.

In summary, multiple errata have been corrected on the ADC, PWM, SPI, SSC, TWI, USART and Voltage regulator. The flash access time increased which might require reprogramming of the number of wait states. The number of write/erase cycles of the Non Volatile Memory bits has been improved.

Identification Method to Distinguish Change:

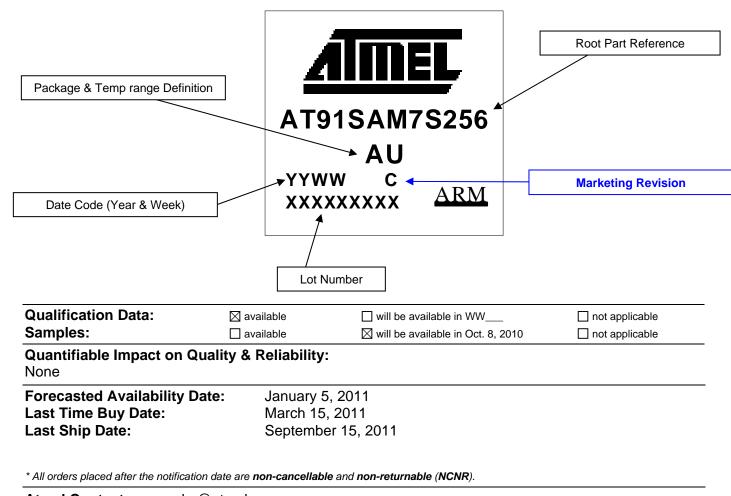
Ordering code, as stated above in the product identification paragraph.

Marketing Revision on the topside marking will be incremented from B to C.

☐ Manufacturing Location

Chip-ID: AT91SAM7S256 Revision C chip ID is: 0x270B0942

Top side marking:



Atmel Contact: pcnadm@atmel.com

Atmel will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice. All correspondence must be sent to the Atmel Contact e-mail address listed above.

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