

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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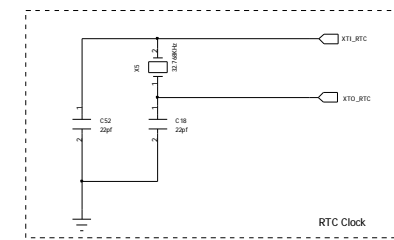
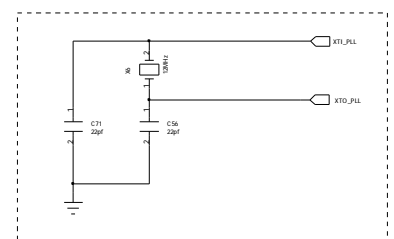
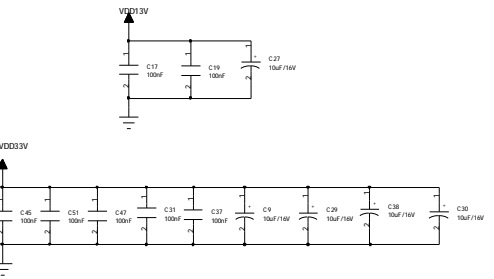
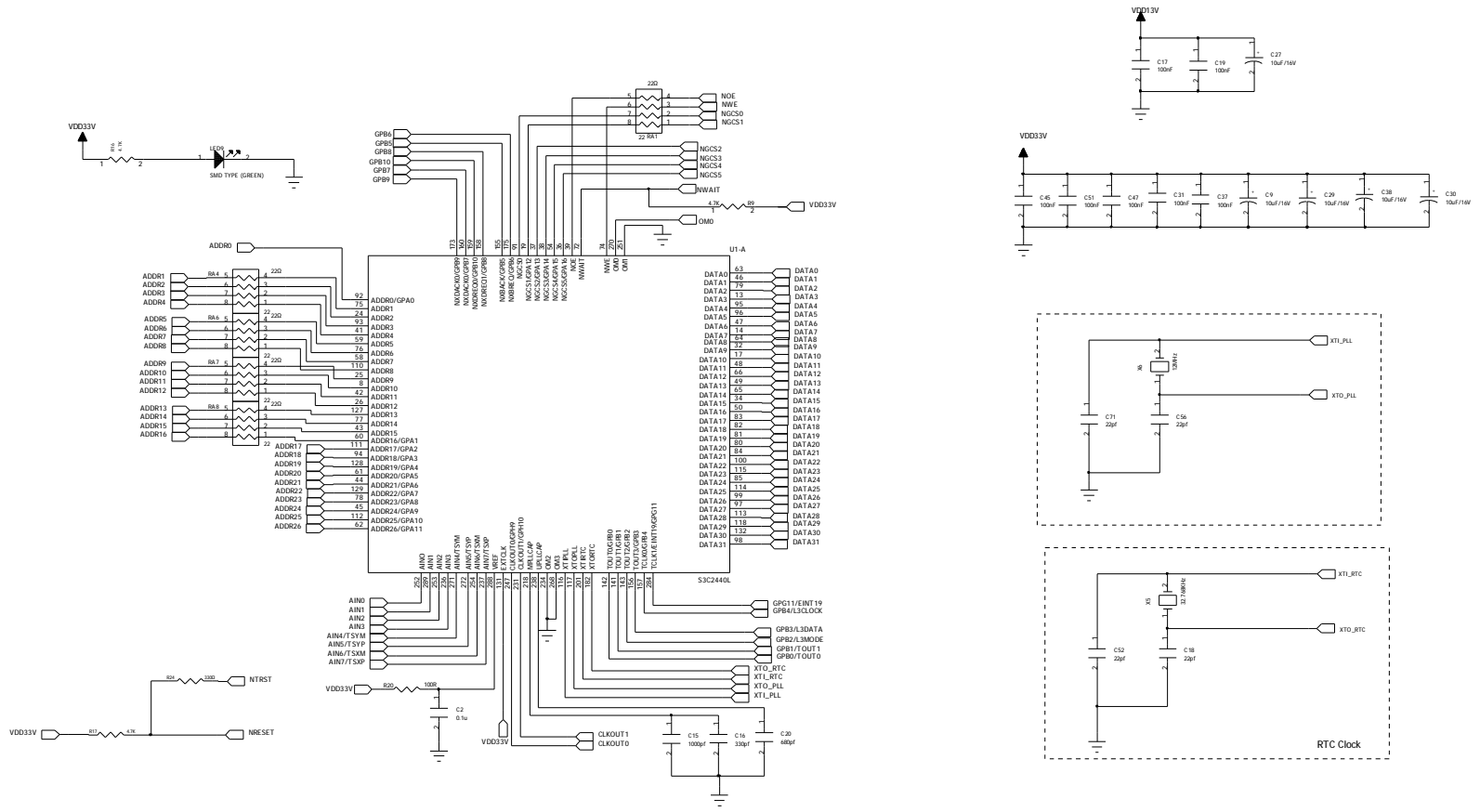
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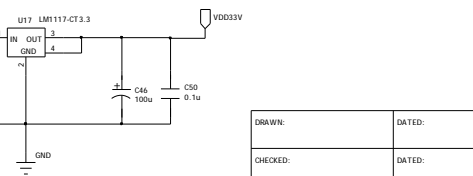
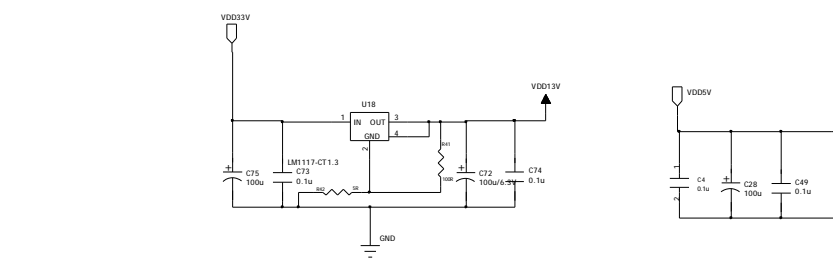
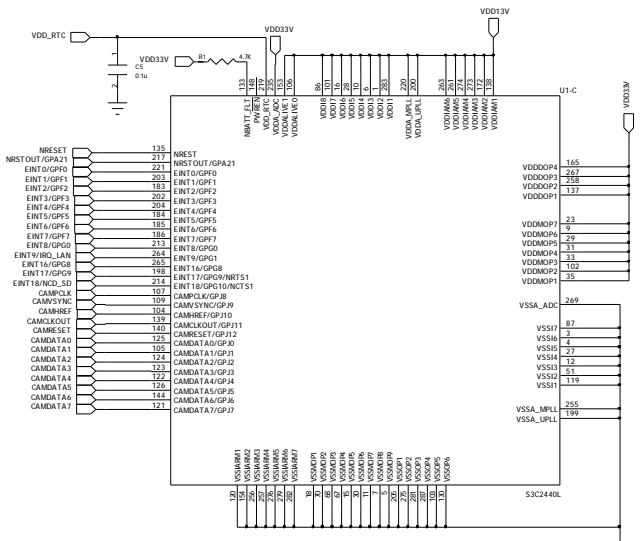
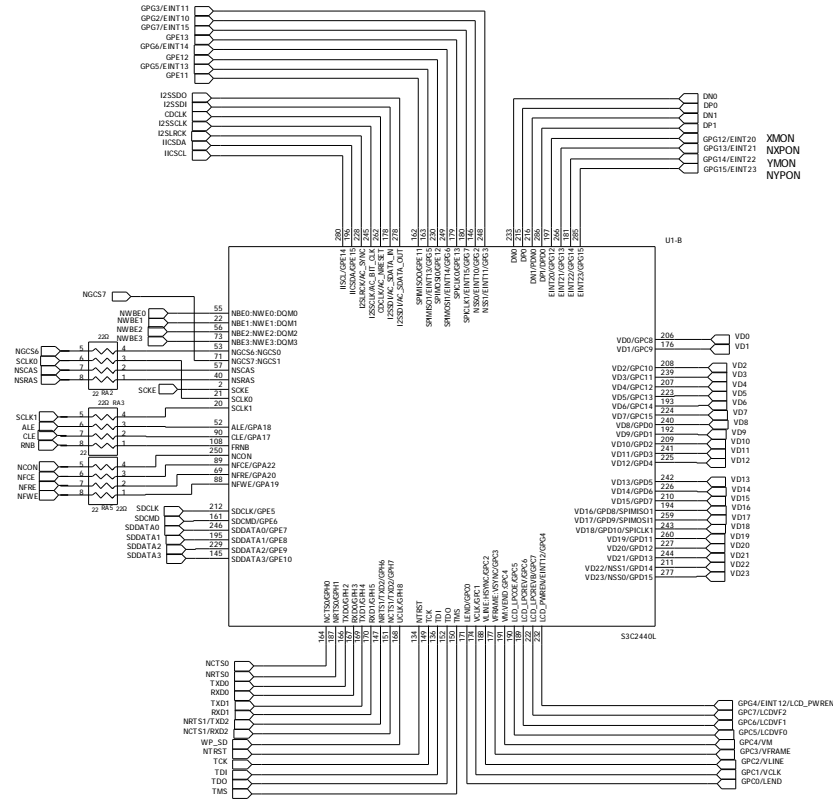
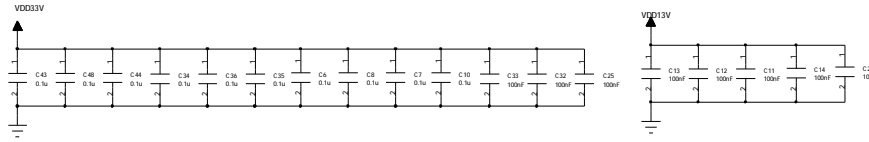
TITLE: 2440核心板

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RELEASED:	DATED:

CODE:	SIZE:	DRAWING NO:	REV:
SCALE:			SHEET: 8/8

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Address Step	NAND Flash Configuration
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1	512 Byte/Page , 4 Step Addressing



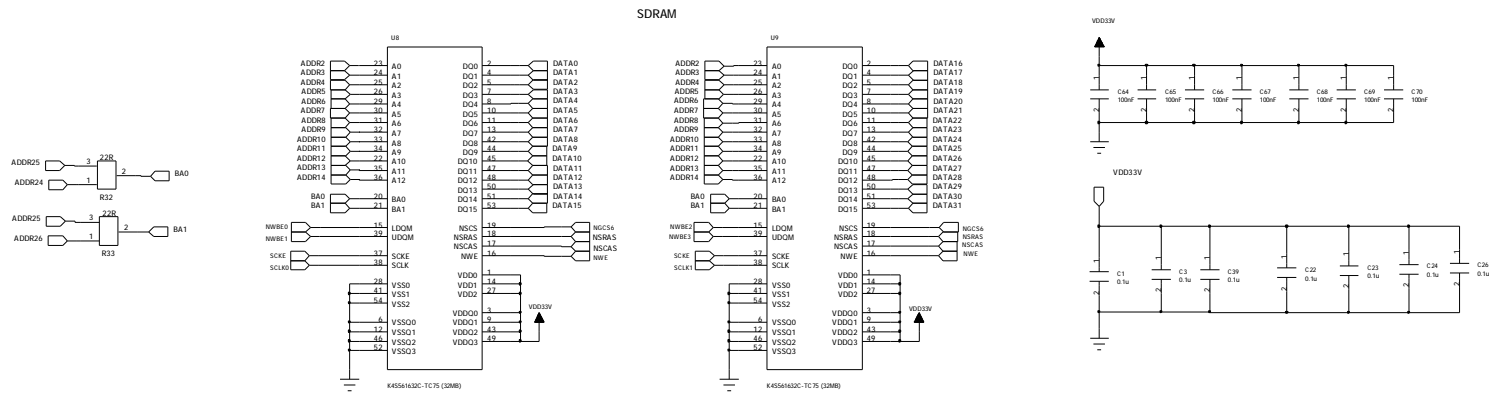
COMPANY: 保定飞凌嵌入式技术有限公司			
TITLE: 2440核心板			
CODE:	SIZE:	DRAWING NO.:	REV:
SCALE:			SHEET: OF

DRAWN:	DATED:
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

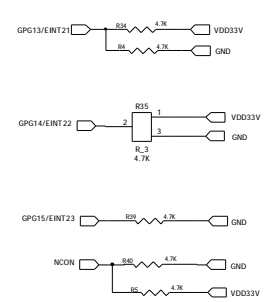
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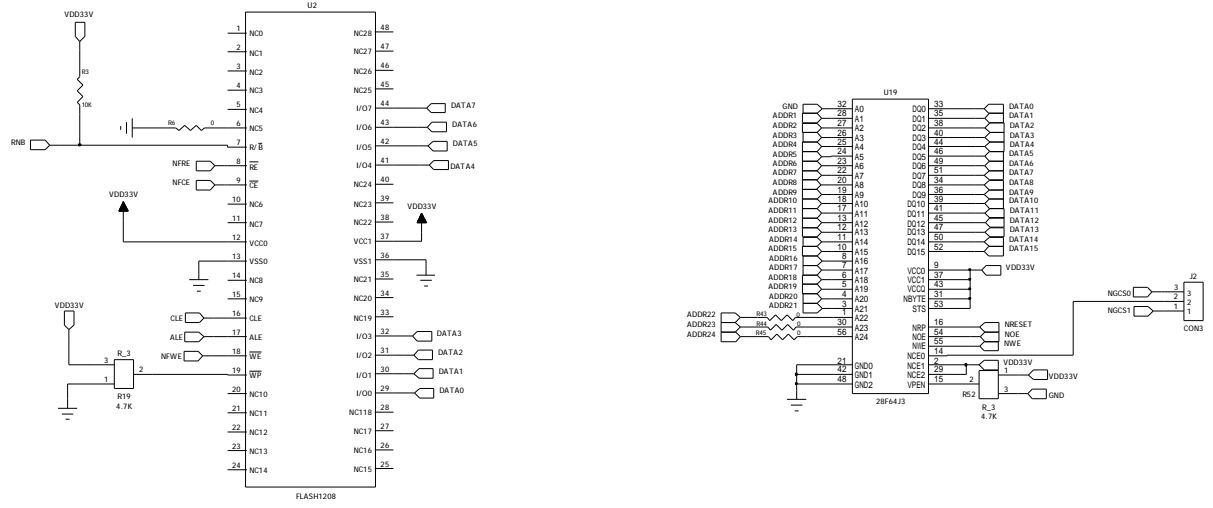
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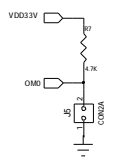
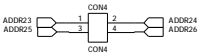
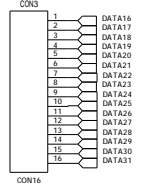
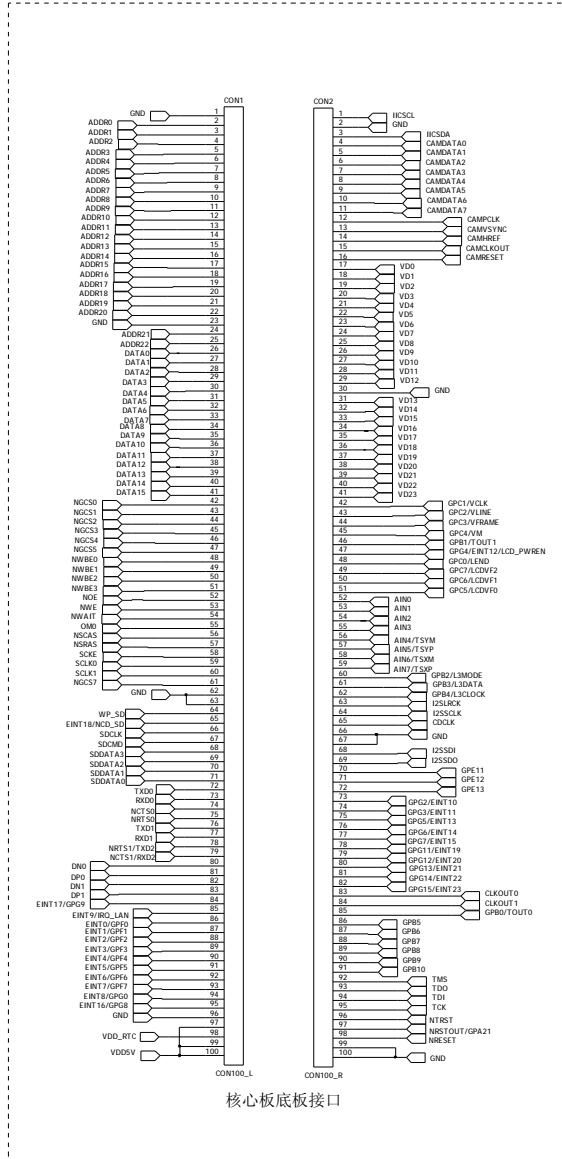
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TITLE: 2440核心板

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REVISION RECORD			
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COMPANY: 保定飞凌嵌入式技术有限公司			
TITLE: 2440核心板			
DRAWN:	DATED:	CODE:	REV:
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QUALITY CONTROL:	DATED:	SCALE:	SHEET: 4 of 4
RELEASED:	DATED:		