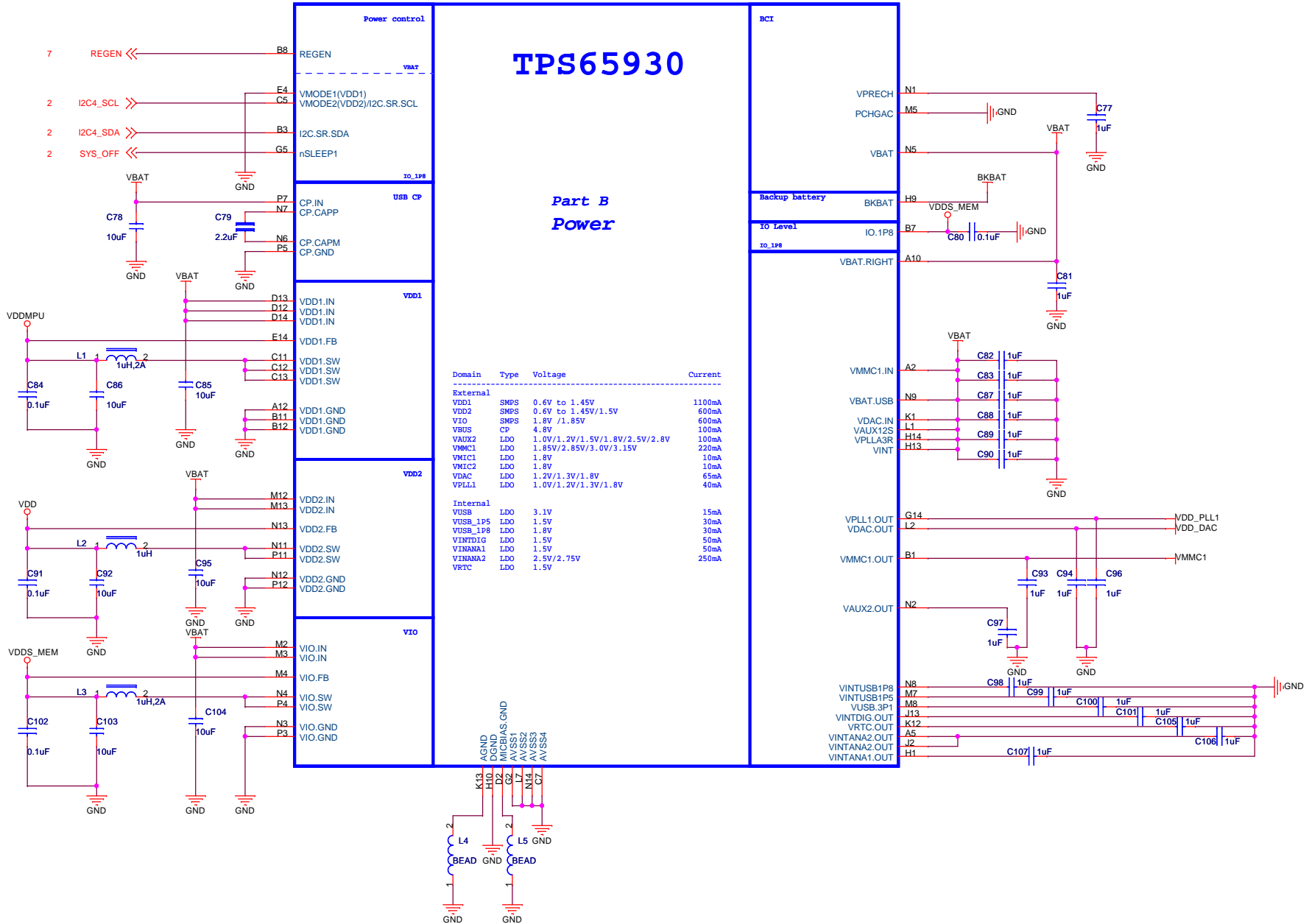
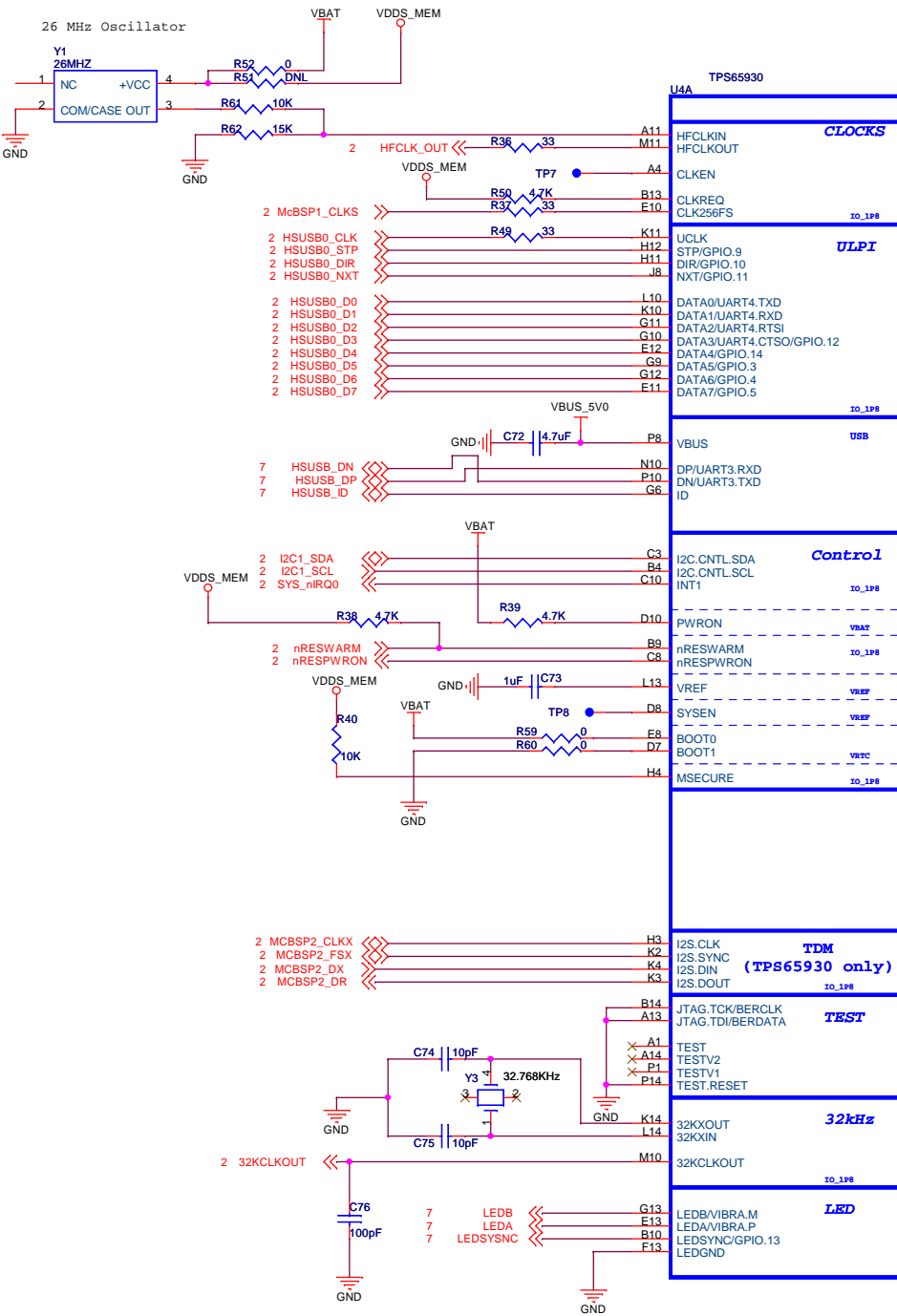


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Part B Power

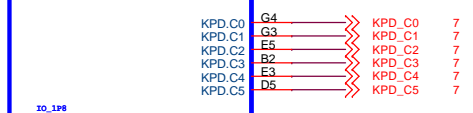
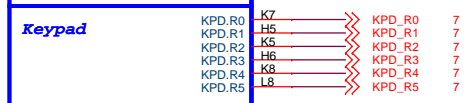
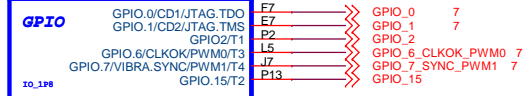
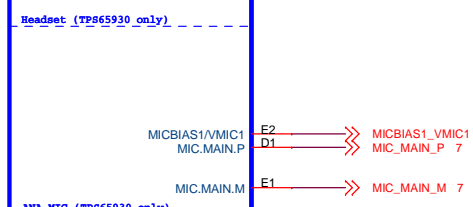
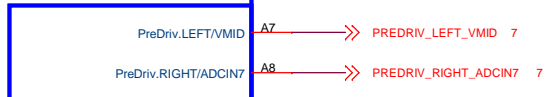
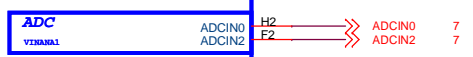
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External			
VDD1	SMPS	0.6V to 1.45V	1100mA
VDD2	SMPS	0.6V to 1.45V/1.5V	600mA
VIO	SMPS	1.8V /1.85V	600mA
VBUS	CP	4.8V	100mA
VDD2	LDO	1.0V/1.2V/1.5V/1.8V/2.5V/2.8V	100mA
VMMC1	LDO	1.85V/2.85V/3.0V/3.15V	220mA
VMIC1	LDO	1.8V	10mA
VMIC2	LDO	1.8V	10mA
VDAC	LDO	1.2V/1.3V/1.8V	65mA
VPLL1	LDO	1.0V/1.2V/1.3V/1.8V	40mA
Internal			
VUSB	LDO	3.1V	15mA
VUSB_IP5	LDO	1.5V	30mA
VUSB_IP8	LDO	1.8V	30mA
VINTDIG	LDO	1.5V	50mA
VINANA1	LDO	1.5V	50mA
VINANA2	LDO	2.5V/2.75V	250mA
VRTC	LDO	1.5V	



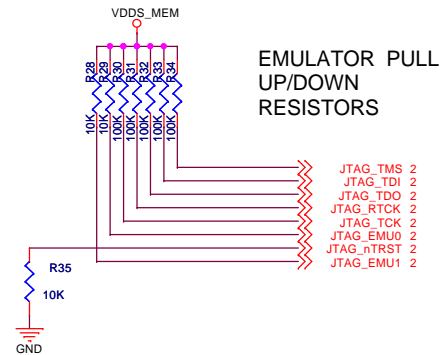
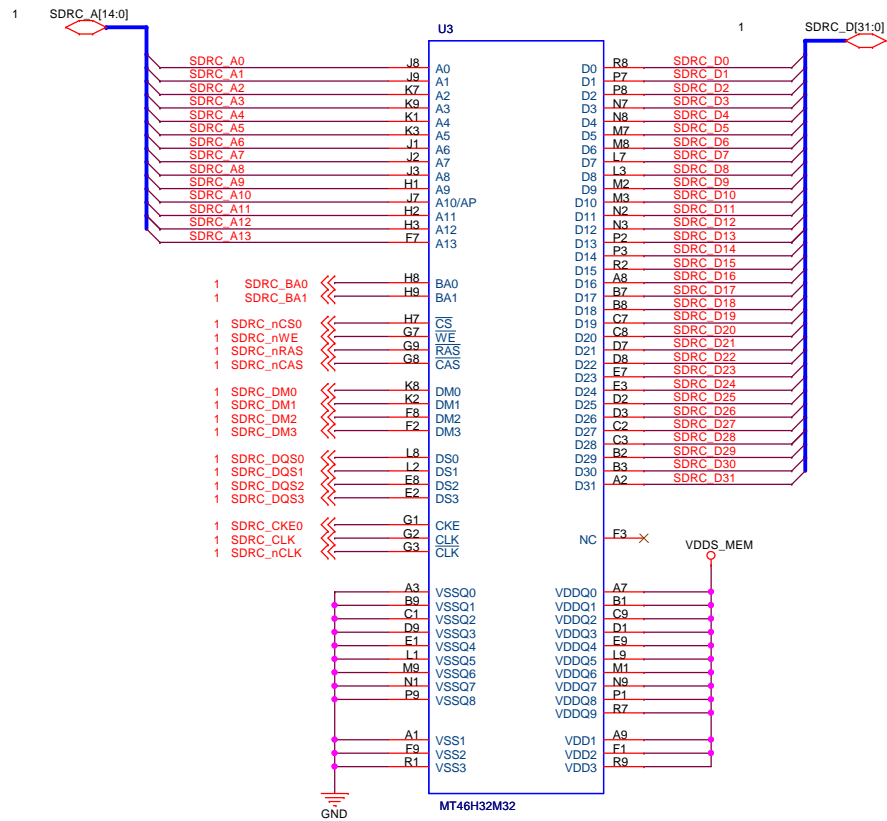


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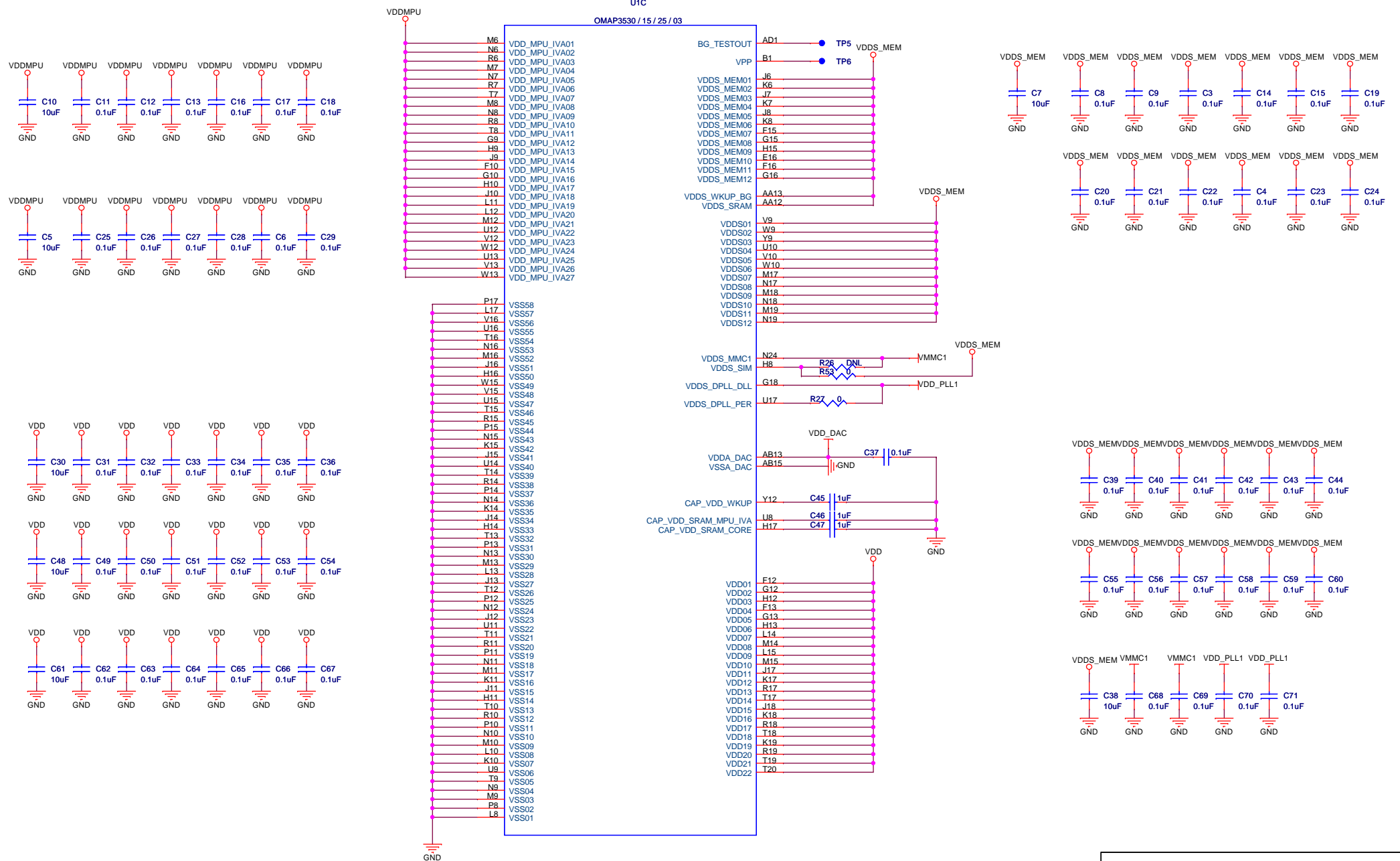
PART A



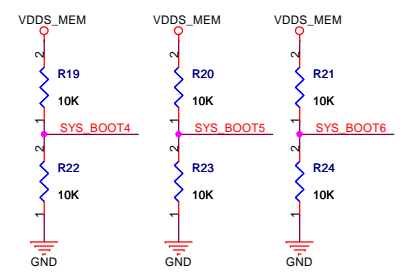
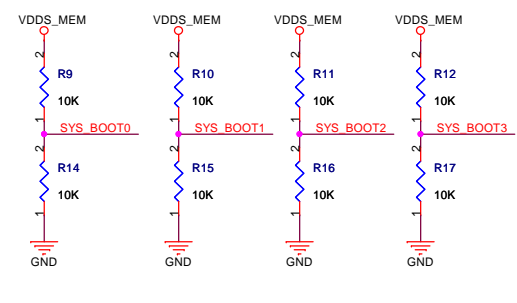
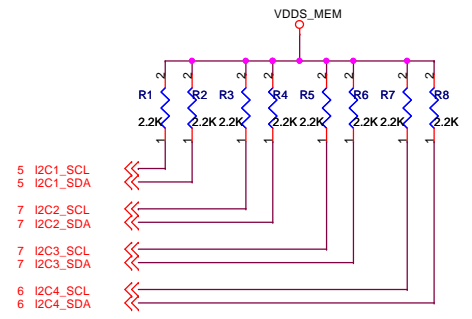
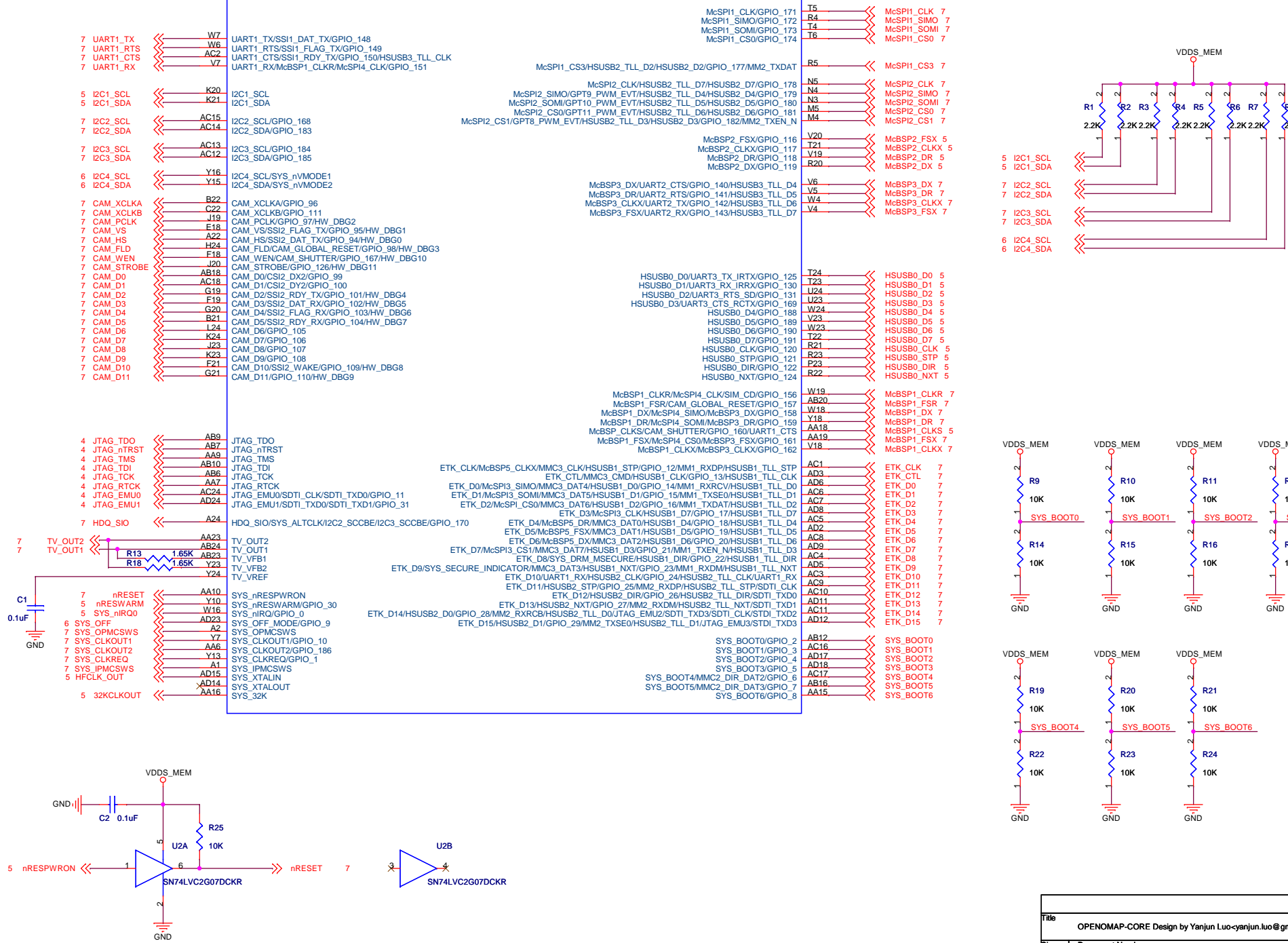
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OPENOMAP-CORE Design by Yanjun Luo-yanjun.luo@gmail.com>		
Size	Document Number	Rev
A3	TPS65930 - PART - A	A00
Date:	Monday, March 16, 2009	Sheet 5 of 7



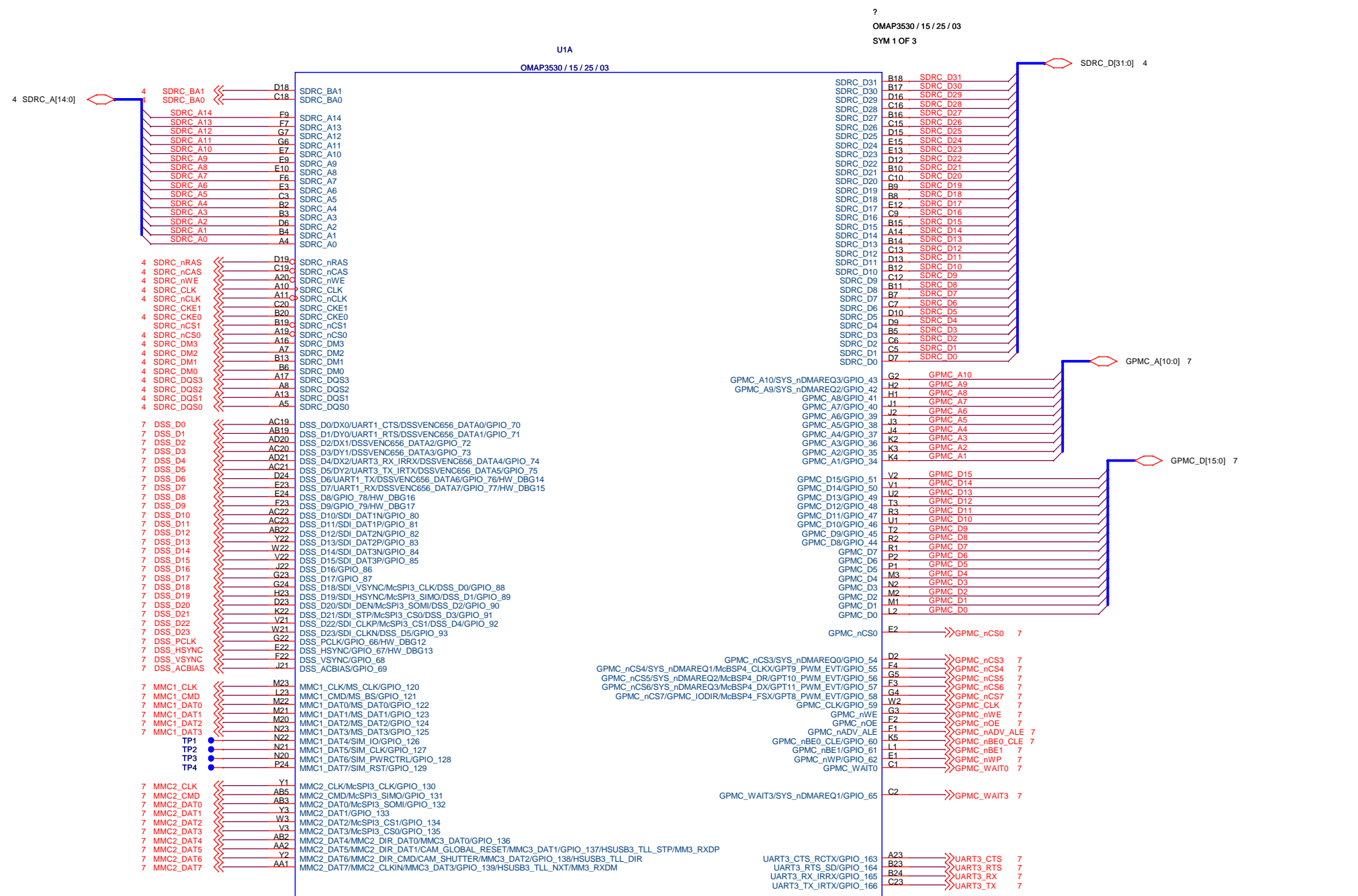
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OPENOMAP-CORE Design by Yanjun Luo-yanjun.luo@gmail.com>		
Size	Document Number	Rev
A3	DDR SDRAM / JTAG	A00
Date:	Saturday, March 14, 2009	Sheet 4 of 7



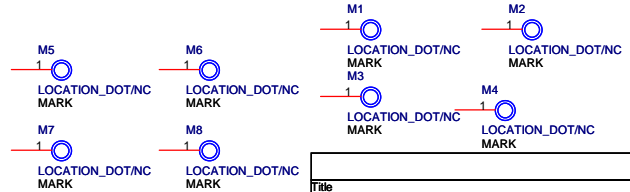
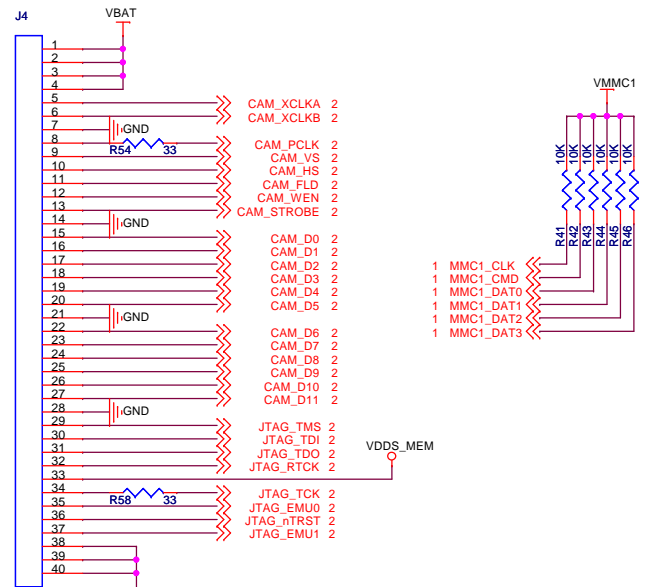
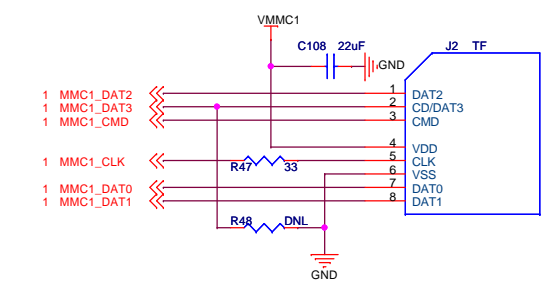
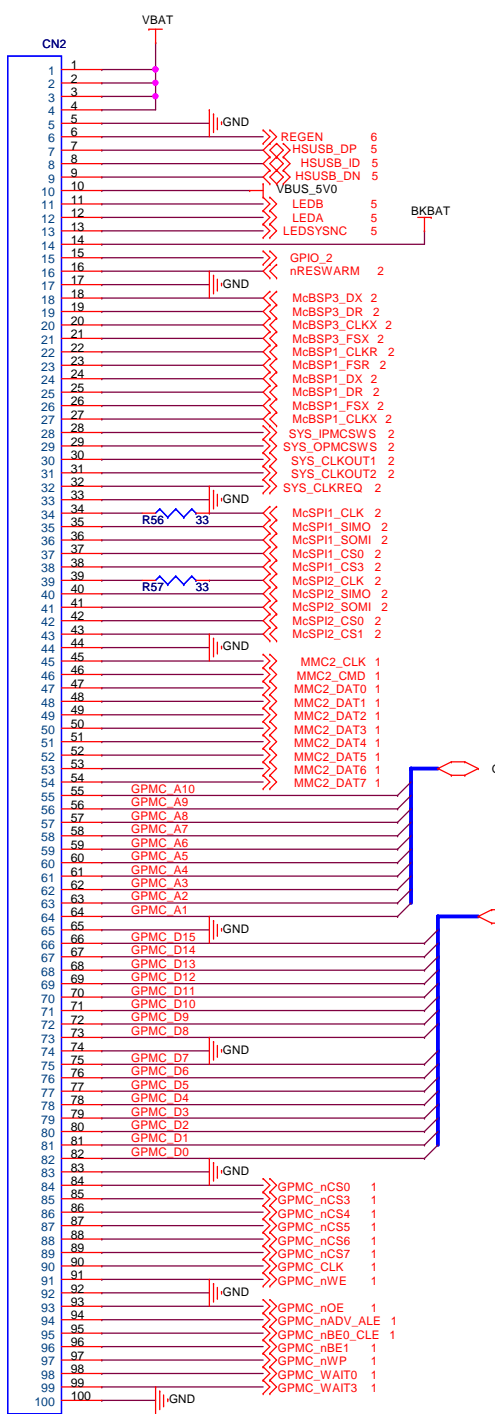
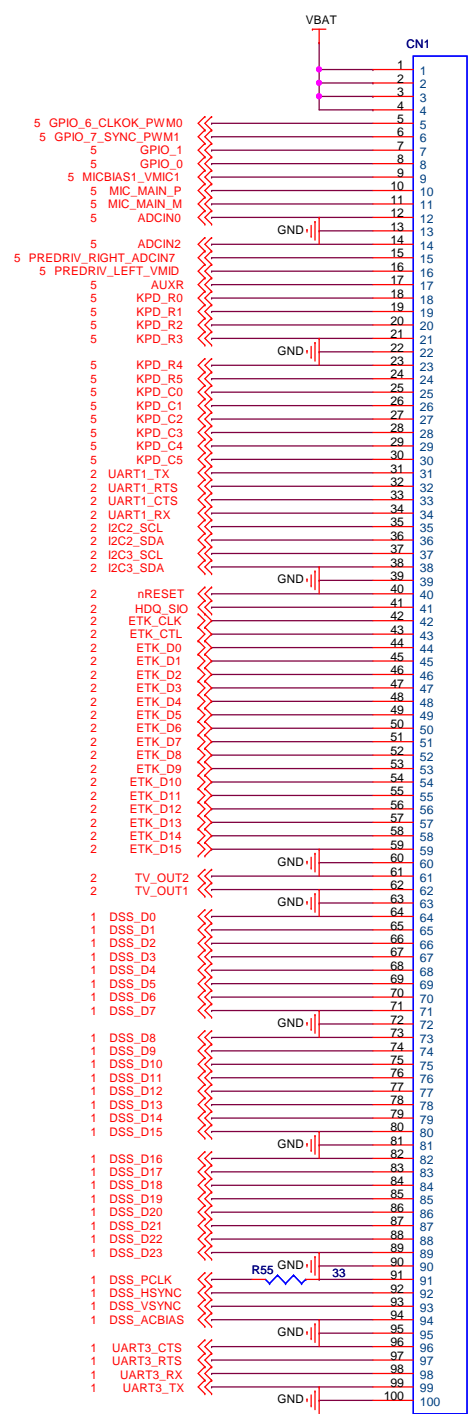
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OPENOMAP-CORE Design by Yanjun Luo-yanjun.luo@gmail.com>		
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A3	OMAP3530 -C- POWER	A00
Date:	Saturday, March 14, 2009	Sheet 3 of 7



Title		
OPENOMAP-CORE Design by Yanjun Luo-yanjun.luo@gmail.com>		
Size	Document Number	Rev
A3	OMAP3530 -B- UART/CAM/TV	A00
Date:	Saturday, March 14, 2009	Sheet 2 of 7



Title		
OPENOMAP-CORE Design by Yanjun Luo-yanjun.luo@gmail.com>		
Size	Document Number	Rev
A3	OMAP3530 -A- BUS/MMC	A00
Date:	Saturday, March 14, 2009	Sheet 1 of 7



Title		
OPENMAP-CORE Design by Yanjun Luo-yanjun.luo@gmail.com		
Size	Document Number	Rev
A3	CONNECTOR 1 / TF	A00
Date:	Monday, April 06, 2009	Sheet 7 of 7