TOSHIBA

SP-870-014

SIOTR2

--Inter-CPU Communication (Simultaneous Transmission/Reception) Using SIO--

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1. Target MCU

This sample program is created targetting at the TLCS-870/X series. When using an MCU other than the TLCS-870/X series, refer to the data sheet for that MCU.

2. Overview

This sample program performs inter-CPU communication (simultaneous transmission/reception) using SIO.

3. Description

The program performs simultaneous transmission/reception (8-byte transmission/reception) using the internal clock. It repeats communication at intervals of 10 ms.

The program uses the following pins:

- Start signal: P20
- Clock: SCK (P43)
- Transmit data: SO (P45)
- Receive data: SI (P44)

Start signal		
0	⇔ 1ms	
Transmit	<u>X D1 X D2 X D3 X D4 X D5 X D6 X D7 X D8 X</u>	
Receiver	<u>X D1 X D2 X D3 X D4 X D5 X D6 X D7 X D8 X</u> 10ms	
	10ms	

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4. Passing Data

The program uses the following variables to pass the operating mode, R/W addresses, and R/W data:

[Variable: GSIOFLG] Communication flag
bit7 FRCVCP =1: Reception completed (cleared by processing routine)
[Variable: GSNDDAT] Transmit data (8 bytes)
[Variable: GRCVDAT] Receive data (8 bytes)

5. Interrupts

- TC2 interrupts (10-ms cycle): To start communication
- TC3 interrupts: To create a start signal
- SBI interrupts

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