BeMicro used together with the Hitex LPC-Prototyping board

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1 Overview

This document describes the usage of a BeMicro FPGA development stick in conjunction with a Hitex LPC-Stick-Prototyping-Board. This prototyping board mainly features space for extension through a breadboard area and spare SMD IC footprints.

The prototype board was developed for the usage with the Hitex LPC stick. So, the signal names in the board documentation refer to pins of the LPC controller. In case BeMicro is connected to the LPC prototype board, the signal names are different.

2 Signals

The tables below describe the overall signal routing starting from the FPGA pins through the edge mount connector finally reaching the connectors X201 and X202. So, the FPGA signals can be probed there and connected to additional components.

In the tables, shaded signal names directly refer to FPGA pin numbers.

X201			
Pin	**Signal Name**	**Pin**	**Signal Name**
1	A12	2	B12
3	B13	4	A14
5	B14	6	A15
7	B16	8	C14
9	C16	10	C15
11	D16	12	D15
13	F15	14	F16
15	G15	16	G16
17	H15	18	H16
19	J16	20	K16
21	K15	22	L16
23	N15	24	N16
25	P15	26	P16
27	P14	28	R16
29	T15	30	R14
31	T14	32	R13
33	T13	34	R12
35	RST_N	36	GND

X202			
Pin	**Signal Name**	**Pin**	**Signal Name**
1	5V	2	5V
3	B10	4	A11
5	B11	6	C9
7	C11	8	A13
9	D12	10	D14
11	E10	12	E11
13	F13	14	F14
15	G11	16	J12
17	J13	18	J14
19	J15	20	K12
21	L15	22	L14
23	L13	24	L11
25	M11	26	N12
27	N14	28	N11
29	R11	30	T11
31	R10	32	T10
33	PWR_N	34	GND
35	3.3V	36	3.3V

Non-shaded signal names denote other signals. These are described below.

Signal Name	Description
3.3V	This net is a supply output of BeMicro.
5V	The net is a supply input for BeMicro.
RST_N	This net is used to reset the application design in the FPGA.
PWR_N	Power enable, low-active.
GND	Signal and power ground.

More information about these signals can be found in the BeMicro user manual.