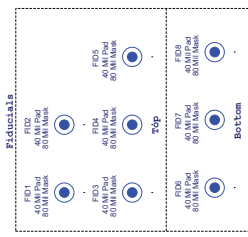
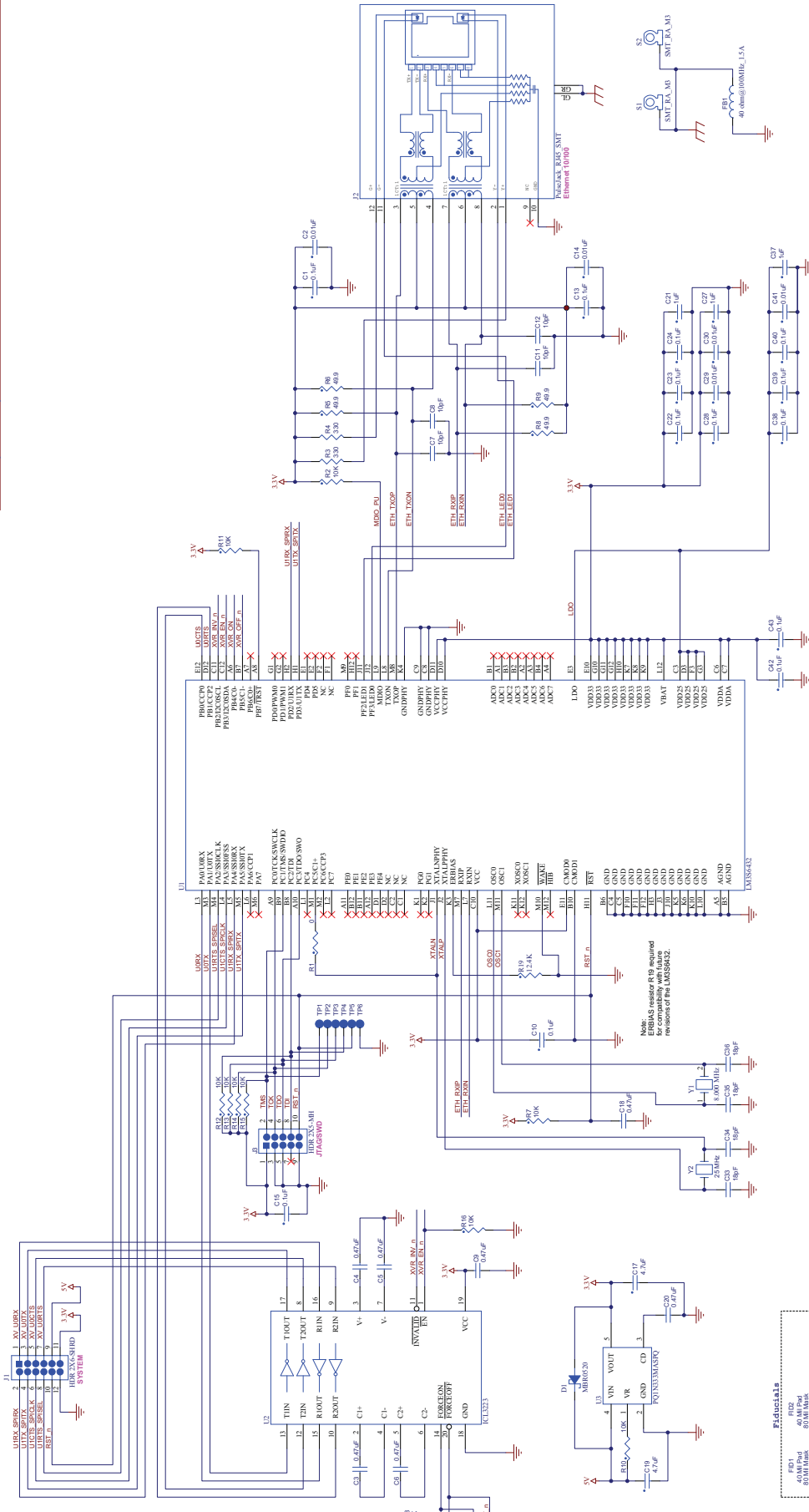


Revision History

Revision	Date	Description
X0	1/16/2008	First release.
X1	1/16/2008	Second release.
X2	1/21/2008	Added JTAG pullups.
X3	1/29/2008	Added pullups to U2 pin 1 to enable operation with ICL3223.
RA	1/29/2008	Released for manufacturing.
RB	2/12/2008	Fixed S1, S2 pads, rotated J3, fixed net names.
RC	4/2/2008	Added pulldown resistor to U1-KC3, added pullups to U2 to enable transceiver after reset. Renamed GNDPHY (U1-K31) to ERBIAS, fixed termination for ethernet RX signals, Added JTAG test pads.



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Document Name: **SERIALJENET Module**
 Page No.:
 Drawn By: Armbh/Cuz
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Size: C
 Document Number: 0001
 Date: 05/23/08
 Sheet: 1 of 1