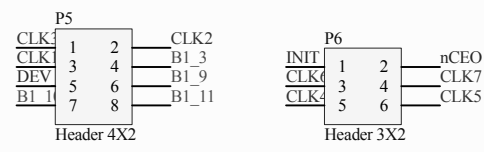
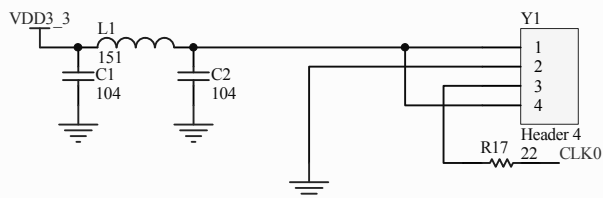
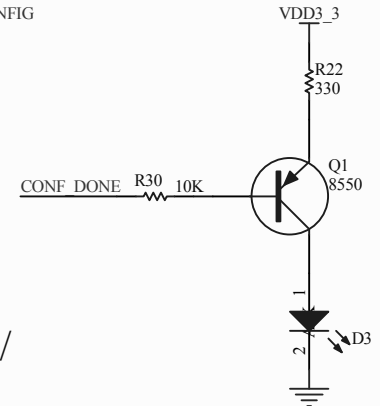
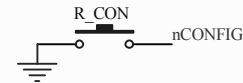
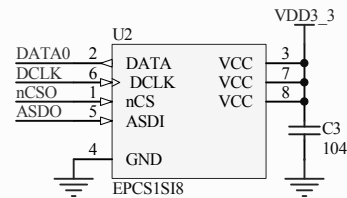
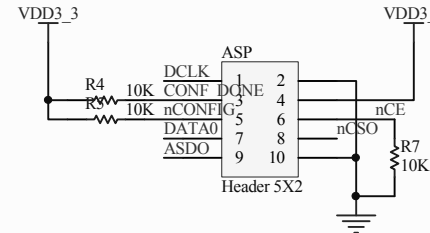
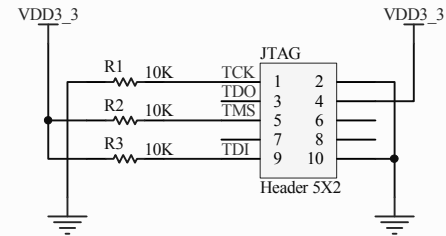
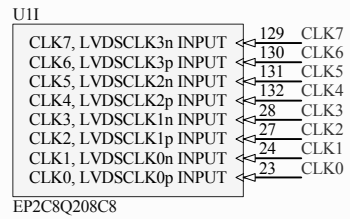
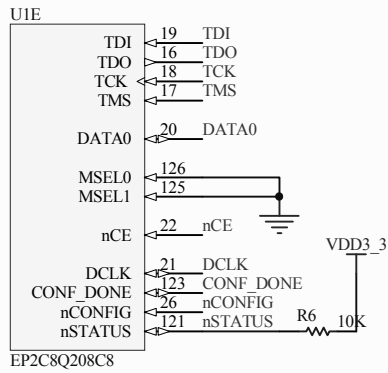


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Date: 2008-9-21	Sheet of	
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bank.SchDoc



U_configure
configure.SchDoc



U_module
module.SchDoc



U_power
power.SchDoc



U_memory
memory.SchDoc



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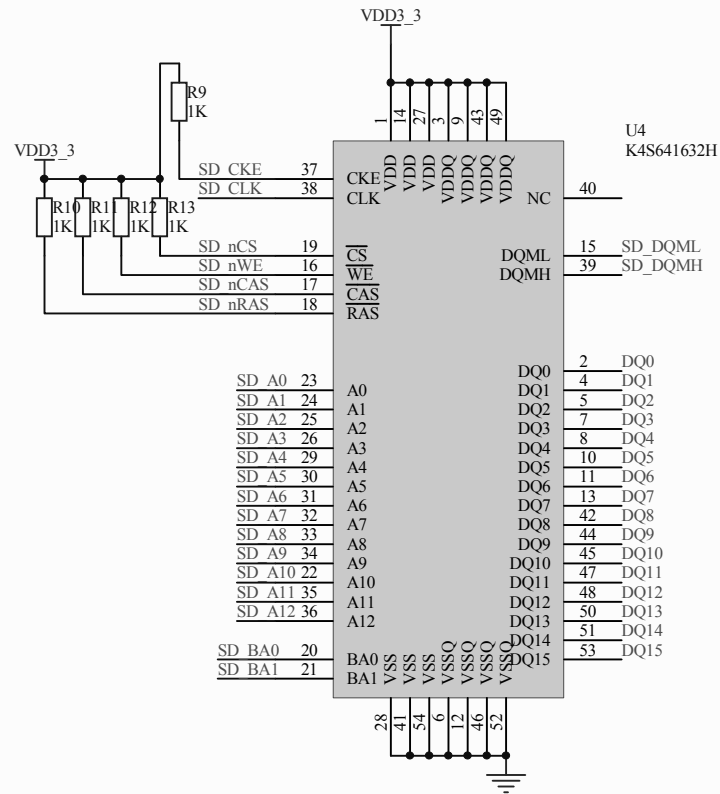
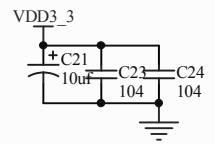
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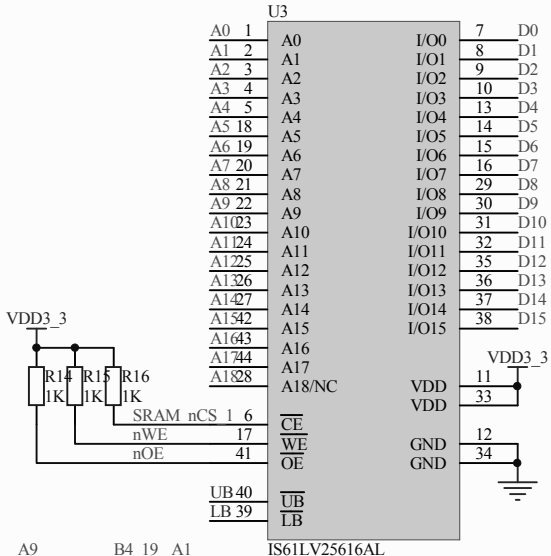
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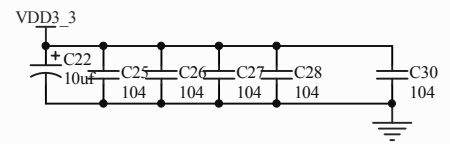
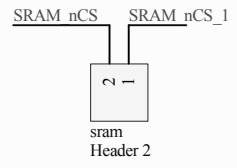
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- B2 25 SD_nWE
- B2 26 SD_DQML
- B2 6 SD_A4
- B2 5 SD_A5
- B2 4 SD_A6
- B2 3 SD_A7
- B2 2 SD_A8
- B2 1 SD_A9
- B3 31 SD_A11
- B3 30 SD_A12
- B3 29 SD_CKE
- B3 28 SD_CLK
- B2 7 DQ8
- B2 8 DQ9
- B2 9 DQ10
- B2 10 DQ11
- B2 11 DQ12
- B2 12 DQ13
- B2 13 DQ14
- B2 14 DQ15
- B2 27 DQ7
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- B2 34 DQ0
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- B2 8 DQ9
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- B2 10 DQ11
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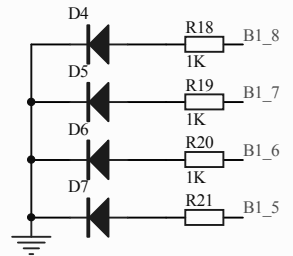


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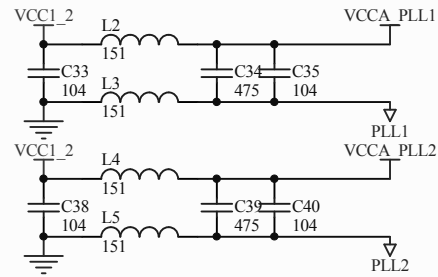
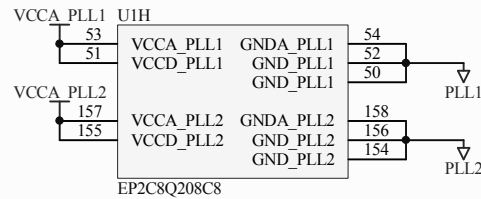
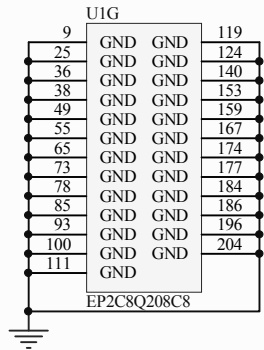
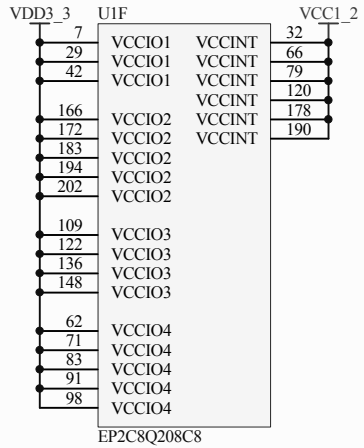
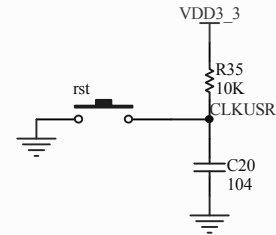
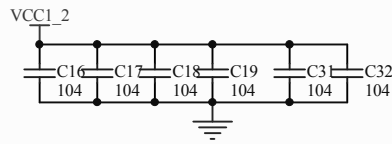
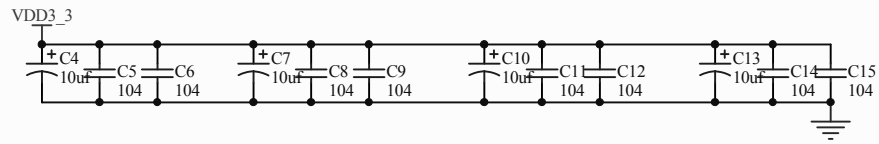


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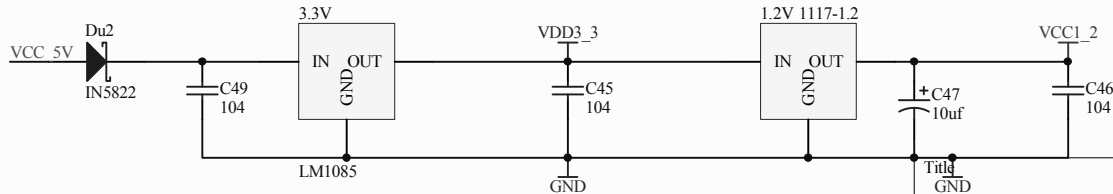
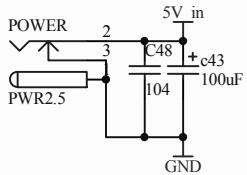
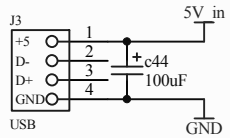
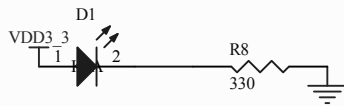
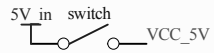


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Date:	2008-9-21	Sheet of
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