

STM32 Connectivity Line Ethernet & TCP/IP introduction

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STM32F107 Ethernet

Generic information about TCP-IP Nichelite TCP-IP Stack Application Examples







The STM32F107 MAC block diagram



- Configurable and flexible MAC 802.3
- Support two industry standard interfaces for the PHY
 - Media Independent Interface (MII)
 - Reduced Media Independent Interface (RMII)
- Integrated DMA controller
- Hardware support of IEEE 1588
- Ethernet wakeup event



Precision Time Protocol (IEEE1588) : What is it



- It is a protocol designed to synchronize real-time clocks of the devices of a network
- Synchronization is done with the most accurate clock found in a packet-based network: called the **Grand Master Clock**.
- Accuracy is Sub-µsecond









STM32F107 Ethernet

Generic information about TCP-IP

Nichelite TCP-IP Stack Application Examples







Generic information about TCP-IP

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TCP-IP : A layered protocol stack



Application Layer	 The main interface for the user. Higher level protocols
Transport Layer	 End-to-end connections (UDP and TCP) Ensure reliability (TCP)
Internet Layer	 Intra & Inter-Network communication no guaranty of delivery
Data link Layer	 Intra-Network communication Transport data on the physical support



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TCP-IP : Key Protocols





TCP-IP : Key Protocols





TCP-IP : Key Application layer protocols







Application layer protocols









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NicheLite from Interniche





NicheLite from Interniche









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Example 1 : User defined protocol over TCP-IP

Hardware Requirements

- Use a MII PHY already qualified by customer
- Minimize the BOM cost

Software Requirements

- The customer has its own protocol
- Data is similar a stream of bytes :
 - Lost packets need to be resent
 - Packets need to be ordered
- Lost data / data reordering is not handled by the protocol





Example 1 : Ethernet Block Diagram





- One 25Mhz external crystal (internal oscillator)
- MII interface
 - 15 pins for the communication between the MAC & PHY
 - 2 pins for the MDC / MDIO (to access PHY registers)

Example 1 : user defined protocol over TCP-IP



Example 2 : Firmware upgrade



Hardware Requirements

- Use RMII to optimize the GPIO usage
- Reduce the BOM cost

Software Requirements

- Need a commercial solution
- Need a cost effective solution
- Need the source code
- Using a simple protocol
- Reliable protocol





Example 2 : Ethernet Block Diagram



RMII interface (RMII + MDC/MDIO = 9 pins)



Or one 50Mhz external oscillator



Example 2 : Firmware upgrade



Summary



The STM32F107 is a product with advanced features

- IEEE1588 support
- MAC IP designed for performances

The STM32F107 is a flexible product :

- RMII and MII connection available on all the packages
- The customer can use his prefered PHY
- Minimum external HW required

ST offers a effective Hardware & Software solution

- NicheLite stack available for download with the Key Protocols and demo to start quickly
- Source code of the stack provided
- <u>www.st.com/stm32</u>



Thank You

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