

UM2455COB-1

Low Power 2.4GHz Transceiver Module

Application Note AN-2455-04

Version: 0.0

Released Date: 2008/7/17

The content of this technical document is subject to change without notice. Please contact UBEC for further information.

All rights are strictly reserved. Any portion of this document shall not be reproduced, copied, or transformed to any other forms without prior permission from Uniband Electronic Corp.



UM2455COB-1

Low Power 2.4GHz Transceiver Module

1. Introduction

The UM2455COB-1 is a low cost, highly integrated 2.4GHz transceiver module designed for low power wireless applications. This module is intended for short-range communications and control operating at the ISM band (2.405 – 2.483.5GHz). The module integrates UM2455 with necessary components and a PCB antenna. The UM2455COB-1 features extensive hardware support of TX/RX FIFO, CSMA-CA, Security engine, MAC functions, clear channel assessment, link quality indication, and wake up trigger by an MCU or a register. The main operating parameters and the 128 bytes transmit/receive FIFOs of the UM2455 can be controlled via the SPI interface. In typical applications, the UM2455 will be used together with a microcontroller and few external passive components.

2 Applications								
<u>Z.</u>	<u>Applications</u>							
	Baby Finder and Tracker		PC Peripherals					
	Home Automation Control		Medical Equipment					
	Interactive Toy		Remote Controller					
	Wireless Sensor Network							
3	Features							
<u> </u>	reatures							
RF.	/Analog							
	ISM band 2.405~2.480 GHz operation		High receiver and RSSI dynamic range					
	-92 dBm sensitivity and 3 dBm maximum input		Support power saving modes					
	level		Low current consumption: 20 mA in RX and 23					
	0 dBm typical output power and 36 dB TX		mA in TX mode					
	power control range		2 uA deep sleep mode					
	Integrated 100 kHz internal oscillator circuit		Data rates of 250 and 625kbps respectively					
MA	C/Baseband							
	O-QPSK modulation (DSSS baseband)							
	Hardware CSMA-CA, automatic ACK response							
	and FCS check							
	Up to 8 nodes supported							
	Four low power operation modes							
	Support all CCA modes and RSSI/LQI							
	Simple four-wire SPI interface							



4. Pin Configuration

4.1. Device Pin Assignments

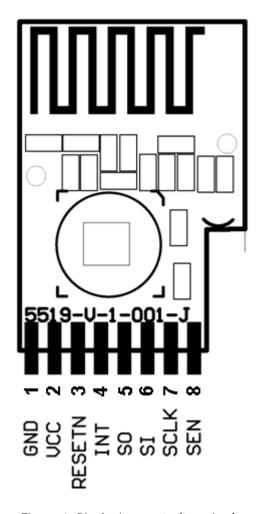


Figure 1. Pin Assignments (top view)



4.2. Device Pin Descriptions

Pin type abbreviation: D = Digital, I = Input, O = Output

Table 1. Pin Descriptions

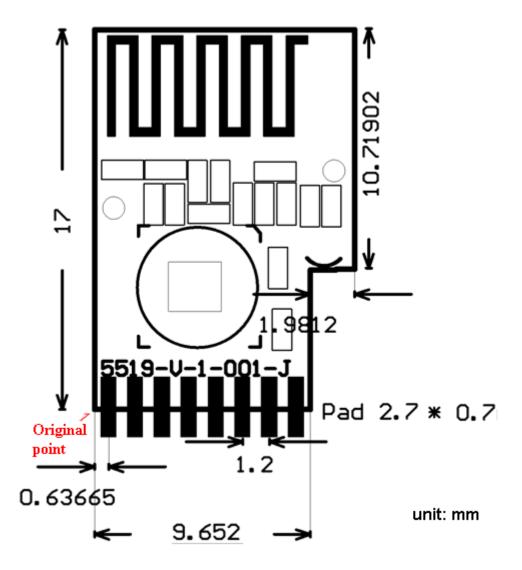
Pin	Symbol	Туре	Description
1	GND	Ground	Ground.
2	VCC	Power	The RF power supply. Bypass with a capacitor as close to the pin as possible.
3	RESETn	DI	Reset
4	INT	DO	The interrupt pin to the micro-processor.
5	SO	DIO	The serial interface data output from the module
6	SI	DIO	The serial interface data input to the module
7	SCLK	DI	The clock of a serial interface.
8	SEN	DI	The enabled pin of a serial interface.



* *Caution*: ESD sensitive. Please refer to Section 2.5 for more information.



4.3. PCB Dimension



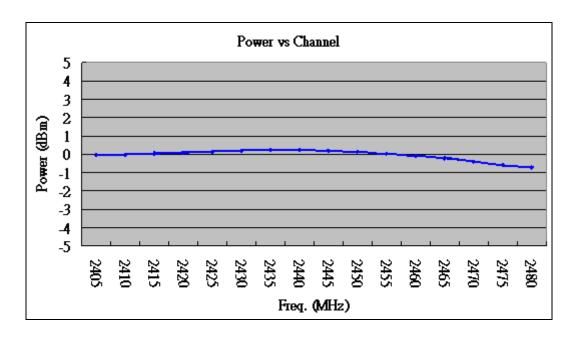
Locating holes dimension:

- (1) (0.8636 mm, 9.017mm) with a radius of 0.508 mm
- (2) (10.7188mm, 10.6934mm) with a radius of 0.508 mm

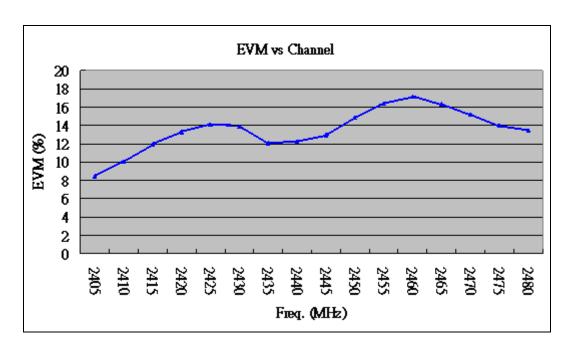


5. Electrical Characteristics

5.1. Tx Output Power

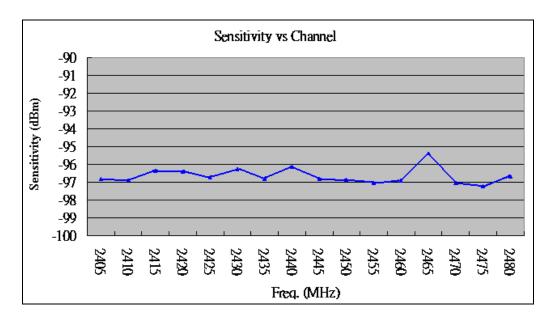


5.2. TX EVM





5.3. RX Sensitivity



5.4. Radio Frequency DC Characteristics

Test conditions: $T_A = 25$ °C, VDD = 3 V

Table 2. DC Electrical Characteristics

Chip Mode	Condition	Min	Тур	Max	Unit
	RF in reset mode.				
IDLE	Regulator, Oscillator, and digital		7.6		mA
	circuits are on.				
STANDBY	All circuits are powered off; only		3.5		uA
STAINDBY	the 100 kHz oscillator is still on.				
DEEP SLEEP	DEEP SLEEP All circuits are powered off.		2		uA
ACTIVE: TX	At 0 dBm, the output power		23		mA
ACTIVE: RX			20		mA

5.5. Peripheral Characteristics

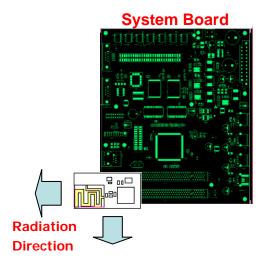
The UM2455COB-1 has the slave mode SPI interface. They can be used by MCU host to access the UM2455 registers and FIFOs. The 4-wire SPI (SEN, SCLK, SI, SO) provides a high speed interface up to 8 MHz on the SCLK pin.

Caution! Hot air gun should not be used after finishing the COB.



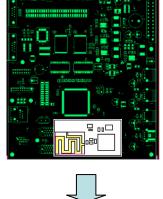
6. Antenna Topology

- (1) Antenna should be placed on the edge of the system.
- (2) Be sure that there is no obstacle (component or ground) present in the radiation direction
- (3) No ground plane or circuit should be put beneath the antenna region of the system boards.

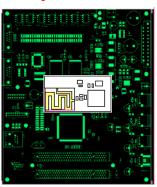


Good Location

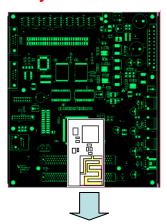
System Board



System Board



System Board



Poor Location



Revision History

Revision	Date	Description of Change
0.0	2008/7/17	Version 0.0 released.



Contact UBEC:

Headquarters

Address: 7F-1, No. 192, Dongguang Rd., Hsinchu, 300 Taiwan

Tel: +886-3-5729898 Fax: +886-3-5718599

Website: http://www.ubec.com.tw

Sales Services

Tel: +886-3-5729898 Fax: +886-3-5718599 E-mail: <u>sales@ubec.com.tw</u>

FAE Services

Tel: +886-3-5729898 Fax: +886-3-5718599 E-mail: <u>fae@ubec.com.tw</u>

DISCLAIMER

ALTHOUGH TO THE BEST KNOWLEDGE OF THE UNIBAND ELECTRONIC CORPORATION (UBEC) THIS DOCUMENT IS ADEQUATE FOR ITS INTENDED PURPOSES, UBEC MAKES NO WARRANTY OF ANY KIND WITH REGARD TO ITS COMPLETENESS AND ACCURACY. UBEC EXPRESSLY DISCLAI MS ANY AND ALL OTHER WARRANTIES, WHETHER EXPRESSED, IMPLIED, OR STATUTORY INCLUDING WITHOUT LIMITATION WARRANTIES OF TITLE, MERCHANTABILITY, NON-INFRINGENT, AND FITNESS FOR A PARTICULAR PURPOSE, WHETHER ARISING IN LAW, CUSTOM, CONDUCT OR OTHERWISE.