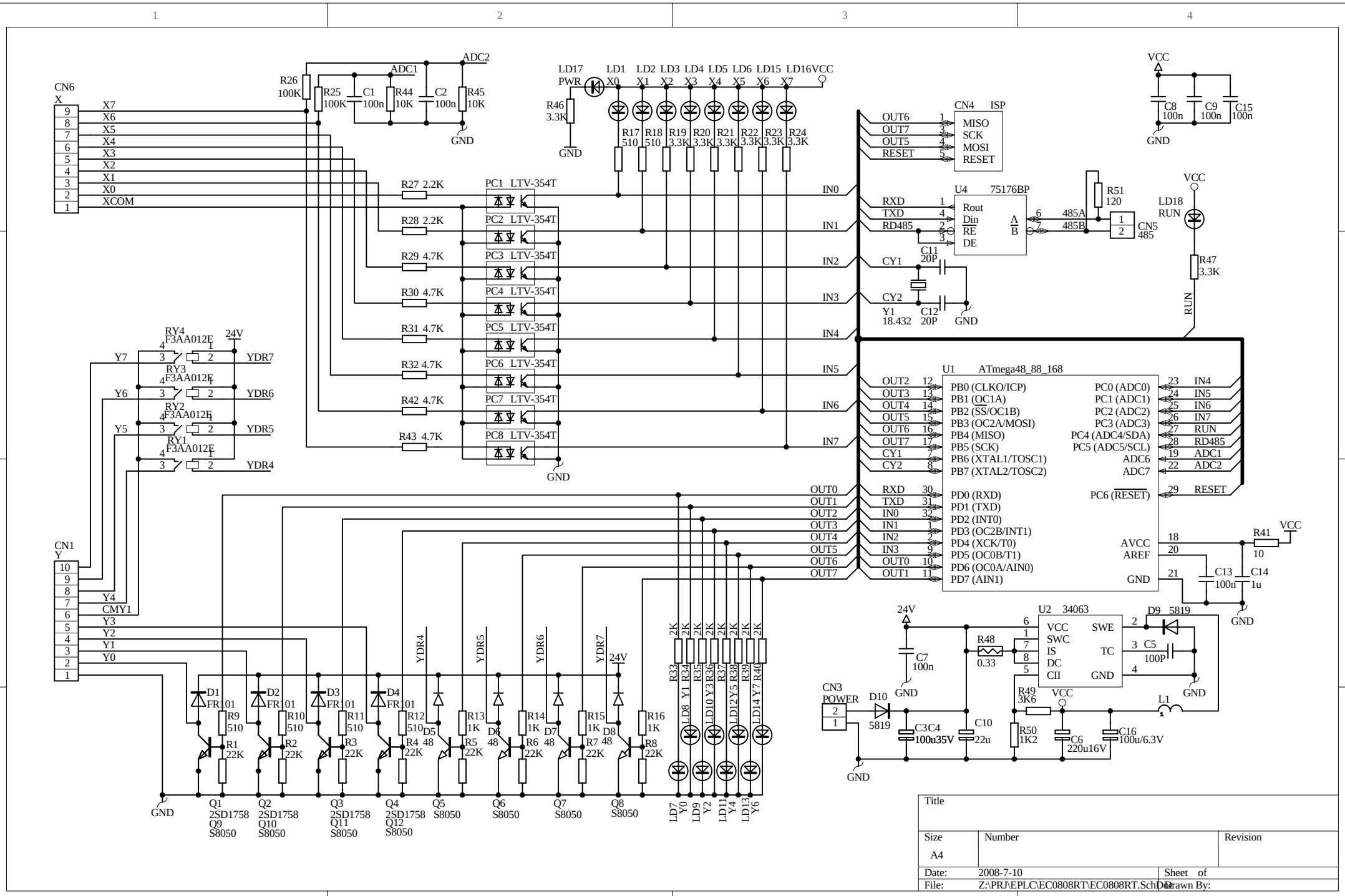
CN6
X
X7
X6
X5
X4
X3
X2
X1
X0
XCOM
R26 100K
R25 100K
C1 100n
R44 10K
C2 100n
R45 10K
ADC1
ADC2
GND
LD17 PWR
R46 3.3K
LD1 LD2 LD3 LD4 LD5 LD6 LD15 LD16
VCC
R17 510
R18 510
R19 3.3K
R20 3.3K
R21 3.3K
R22 3.3K
R23 3.3K
R24 3.3K
R27 2.2K
R28 2.2K
R29 4.7K
R30 4.7K
R31 4.7K
R32 4.7K
R42 4.7K
R43 4.7K
PC1 LTV-354T
PC2 LTV-354T
PC3 LTV-354T
PC4 LTV-354T
PC5 LTV-354T
PC6 LTV-354T
PC7 LTV-354T
PC8 LTV-354T
IN0
IN1
IN2
IN3
IN4
IN5
IN6
IN7
CN4 ISP
MISO
SCK
MOSI
RESET
OUT6
OUT7
OUT5
RESET
C8 100n
C9 100n
C15 100n
U4 75176BP
Rout
Din
RE
DE
A
B
RXD
TXD
RD485
CY1
CY2
485A
485B
R51 120
CN5 485
C11 20P
C12 20P
Y1 18.432
LD18 RUN
R47 3.3K
RUN
RY4 F3AA012E
RY3 F3AA012E
RY2 F3AA012E
RY1 F3AA012E
24V
Y7
Y6
Y5
YDR7
YDR6
YDR5
YDR4
U1 ATmega48_88_168
PB0 (CLKO/ICP)
PB1 (OC1A)
PB2 (SS/OC1B)
PB3 (OC2A/MOSI)
PB4 (MISO)
PB5 (SCK)
PB6 (XTAL1/TOSC1)
PB7 (XTAL2/TOSC2)
PD0 (RXD)
PD1 (TXD)
PD2 (INT0)
PD3 (OC2B/INT1)
PD4 (XCK/T0)
PD5 (OC0B/T1)
PD6 (OC0A/AIN0)
PD7 (AIN1)
PC0 (ADC0)
PC1 (ADC1)
PC2 (ADC2)
PC3 (ADC3)
PC4 (ADC4/SDA)
PC5 (ADC5/SCL)
ADC6
ADC7
PC6 (RESET)
AVCC
AREF
GND
OUT0
OUT1
OUT2
OUT3
OUT4
OUT5
OUT6
OUT7
CN1
Y
Y4
CMY1
Y3
Y2
Y1
Y0
D1 FR101
D2 FR101
D3 FR101
D4 FR101
R9 510
R10 510
R11 510
R12 510
R1 22K
R2 22K
R3 22K
R4 22K
D5 48
D6 48
D7 48
D8 48
R13 1K
R14 1K
R15 1K
R16 1K
R5 22K
R6 22K
R7 22K
R8 22K
R33 2K
R34 2K
R35 2K
R36 2K
R37 2K
R38 2K
R39 2K
R40 2K
LD8 Y1
LD10 Y3
LD12 Y5
LD14 Y7
LD7 Y0
LD9 Y2
LD11 Y4
LD13 Y6
Q1 2SD1758 Q9 S8050
Q2 2SD1758 Q10 S8050
Q3 2SD1758 Q11 S8050
Q4 2SD1758 Q12 S8050
Q5 S8050
Q6 S8050
Q7 S8050
Q8 S8050
R41 10
C13 100n
C14 1u
U2 34063
VCC
SWC
IS
DC
CII
SWE
TC
GND
D9 5819
C5 100P
R48 0.33
C7 100n
CN3 POWER
D10 5819
C3C4 100u35V
C10 22u
R49 3K6
R50 1K2
C6 220u16V
C16 100u/6.3V
L1
Title
Size
A4
Number
Revision
Date: 2008-7-10
Sheet of
File: Z:\PRJ\EPLC\EC0808RT\EC0808RT.SchDoc
Drawn By: