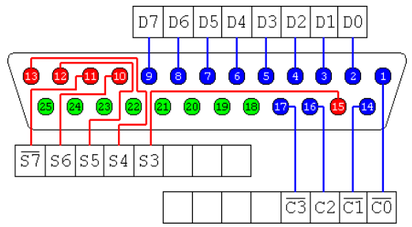
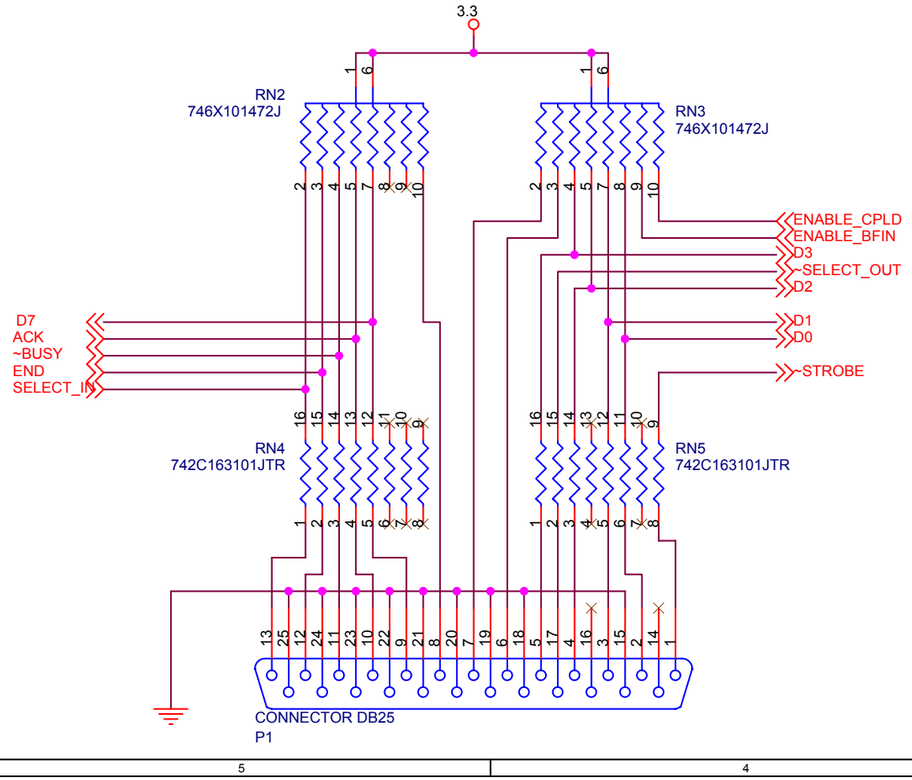
STAMP JTAG Interface Cable

PC Parallel Port Interface

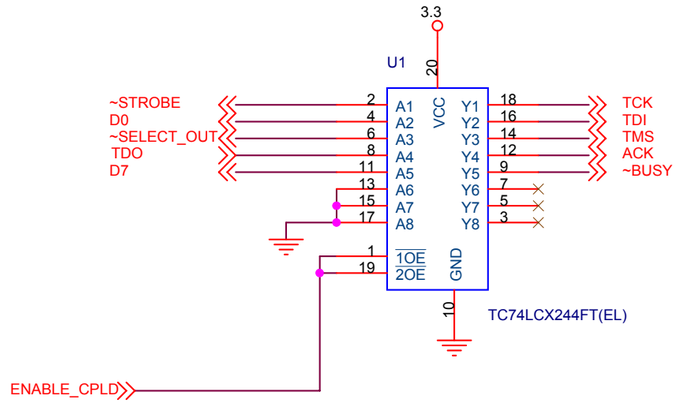
Signal Name	Register Bit	DB-25 Pin	I/O Direction
-Strobe	~C0	1	Output
+Data Bit 0	D0	2	Output
+Data Bit 1	D1	3	Output
+Data Bit 2	D2	4	Output
+Data Bit 3	D3	5	Output
+Data Bit 4	D4	6	Output
+Data Bit 5	D5	7	Output
+Data Bit 6	D6	8	Output
+Data Bit 7	D7	9	Output
-Acknowledge	S6	10	Input
+Busy	~S7	11	Input
+Paper End	S5	12	Input
+Select In	S4	13	Input
-Auto Feed	~C1	14	Output
-Error	S3	15	Input
-Initialize	C2	16	Output
-Select	~C3	17	Output
Ground	-	18-25	-



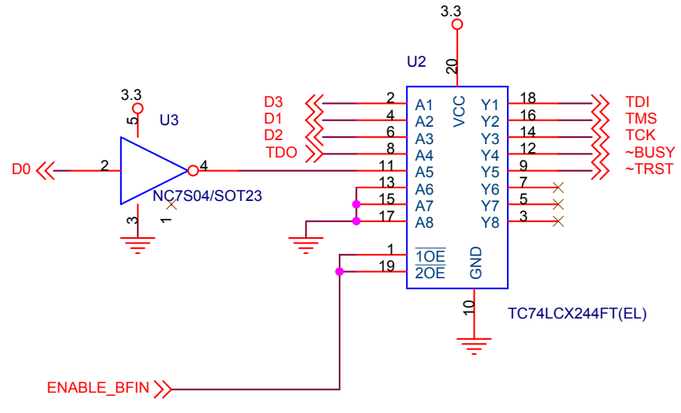
7	6	5	4	3	2	1	0	I/O Port	
D7	D6	D5	D4	D3	D2	D1	D0	(Data)	Base = 0x278/378/3BC
~S7	S6	S5	S4	S3				(Status)	Base +1
				~C3	C2	~C1	~C0	(Control)	Base +2



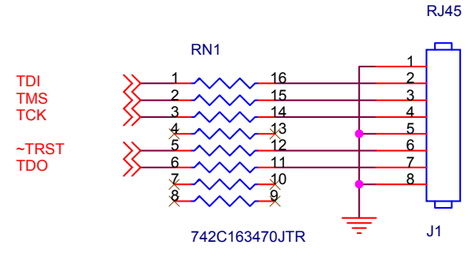
In Circuit Programming for Atmel ATF1504



Flash Programming via Blackfin JTAG

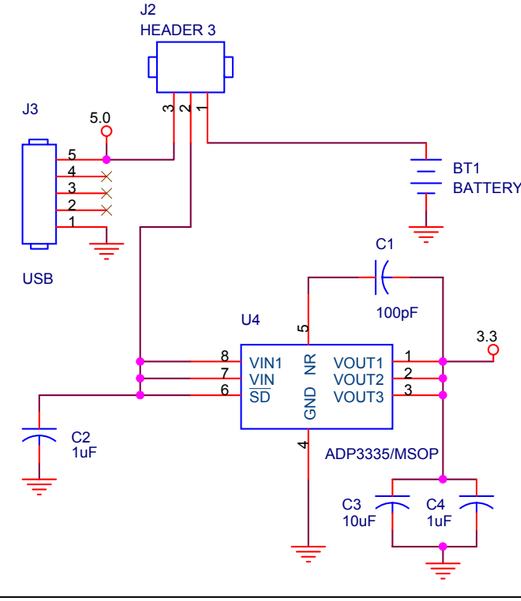


Select Blackfin or CPLD



Designed for Standard, Straight-Through Wiring
(both ends are the same)

RJ45 Pin #	Wire Colour	Signal Name	Input/ Output	Description
1	White/Orange	GND	Ground	
2	Orange	TDI	Input	Test Data Input
3	White/Green	TMS	Input	Test Mode Select
4	Blue	TCK	Input	Test Clock
5	White/Blue	GND	Ground	
6	Green	~TRST	Input	Test Reset
7	White/Brown	TDO	Output	Test Data Out
8	Brown	GND	Ground	



For Help try http://blackfin.uclinux.org/projects/stamp

Title	BF533/2/1 STAMP Board	
Size: B	Document Number: STAMP JTAG Connectors	Rev: 0.8
Date: Thursday, July 15, 2004	Sheet 1 of 1	