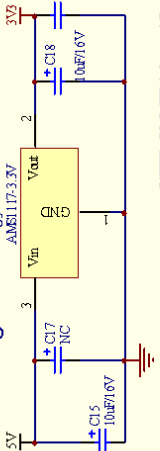


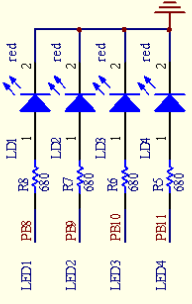
Power regulator



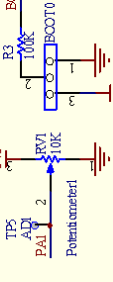
STM32F10x-48

PA0	Wkup/USART2_CS/ADC_IN0/TIM6_CH1_ETR
PA1	USART2_RS48DC_IN0/TIM6_CH2
PA2	USART2_TX/ADC_IN0/TIM6_CH3
PA3	USART2_RX/ADC_IN0/TIM6_CH4
PA4	NS/USART1_2_CK/ADC_IN4
PA5	SP1_SCK/ADC_IN5
PA6	SP1_MISO/ADC_IN6/TIM6_CH5
PA7	SP1_MOSI/ADC_IN7/TIM6_CH6
PA8	USART1_CK/TIM1_CH1/MCO
PA9	USART1_TX/TIM1_CH2
PA10	USART1_RX/TIM1_CH3
PA11	USART1_RS48DC2/USBDM(2)/TIM1_ETR
PA12	USART1_RS48DC1/USBDM(1)/TIM1_ETR
PA13	SWDIO
PA14	SWCLK
PA15	SDIO
PB0	PA/ADC_IN0/TIM6_CH8
PB1	PA/ADC_IN0/TIM6_CH9
PB2	BOOT0
PB3	BOOT1
PB4	BOOT2
PB5	BOOT3
PB6	BOOT4
PB7	BOOT5
PB8	LD1
PB9	LD2
PB10	LD3
PB11	LD4
PB12	TPS4901
PB13	TPS4901
PB14	TPS4901
PB15	TPS4901
VBAT	VBAT

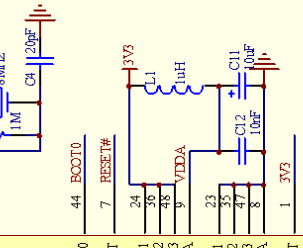
User LED



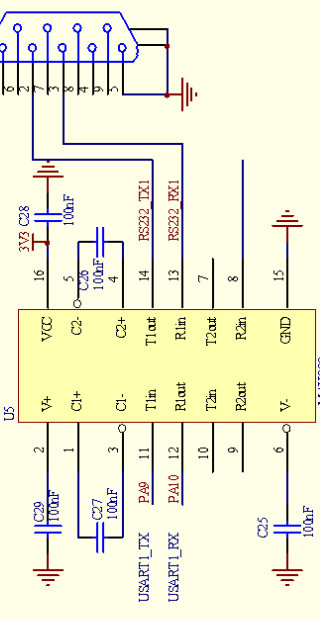
Potentiometer



CPU Core



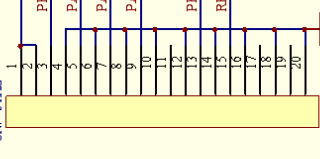
USART1/2 connector



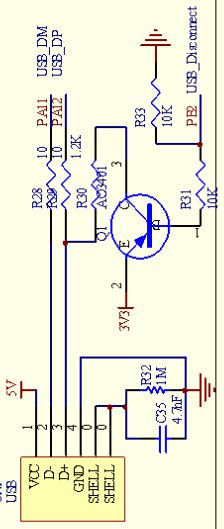
CN13

1	P20
2	P21
3	P22
4	P23
5	P24
6	P25
7	P26
8	P27
9	P28
10	P29
11	P30
12	P31
13	P32
14	P33
15	P34
16	P35
17	P36
18	P37
19	P38
20	P39
21	P40
22	P41
23	P42
24	P43
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31	P50
32	P51
33	P52
34	P53

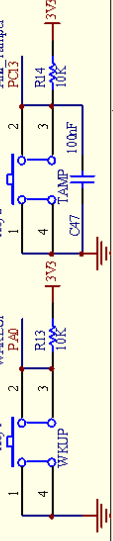
JTAG connector



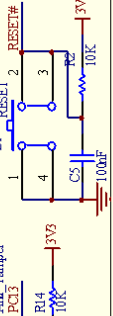
USB



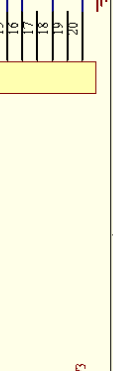
Wakeup Button



Tamper Button



Reset Button



MN-STM32-48EK

Title	Size	Number	Revision
	A4		
Date	23. Apr. 2008	Sheet of	4
File	E:\WORK\BAK\STM32_48\PINno.40\PINno.40.DDB		