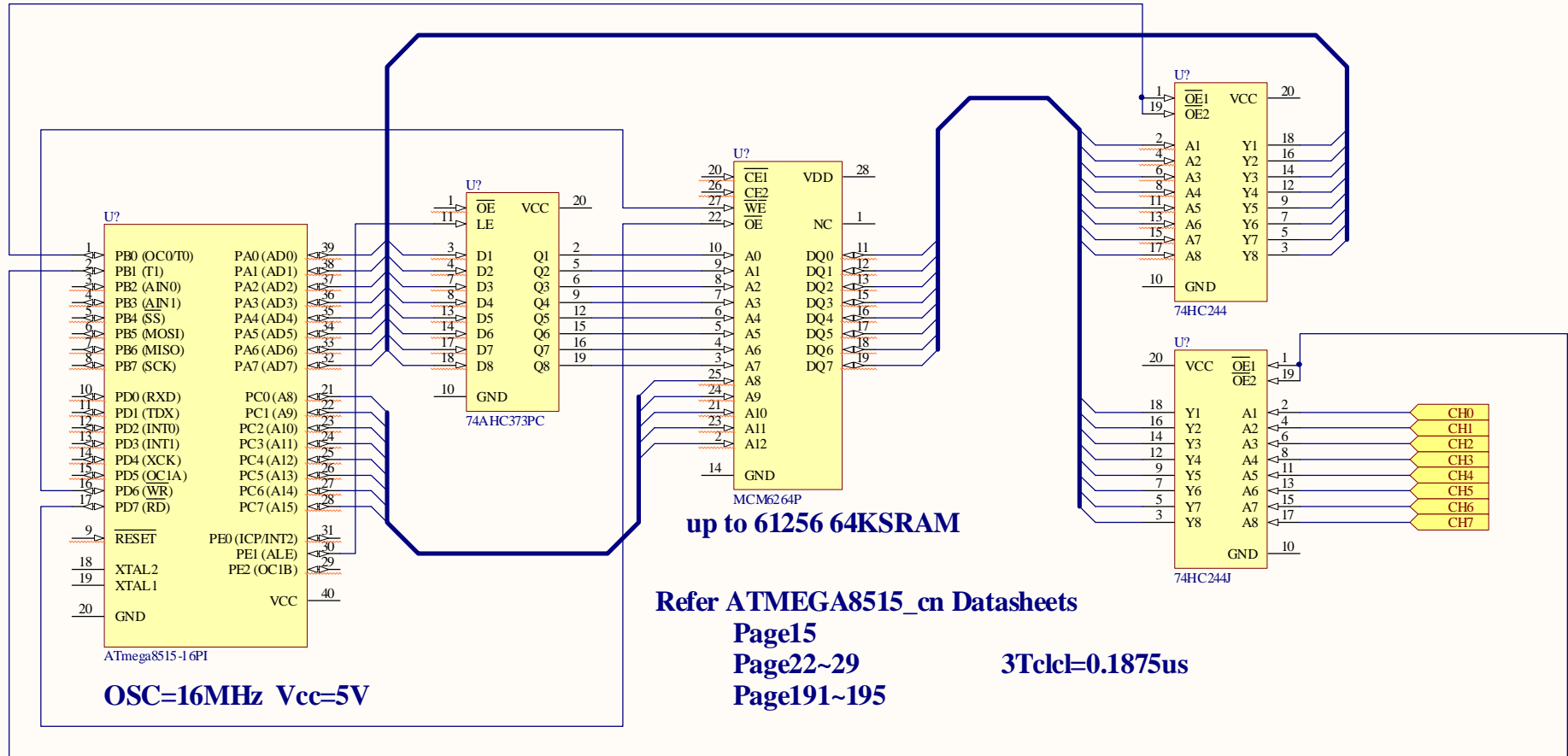


# 4Msps 8 Channels Logic Analyser Hardware (Simply Hardware ,Un-Test )

**Read Memory and Send to PC**



**Capture Singal and Write to Memory**

Title		
Size A4	Number	Revision
Date: 2004-11-20	Sheet of	
File: D:\Protel_DXP2004\SRAM.SchDoc	Drawn By:	<b>HJJourAVR</b>